

RKZ6.8Z4MFAKT

Silicon Planar Zener Diode for Surge Absorption

REJ03G1351-0100
Rev.1.00
Feb 22, 2006

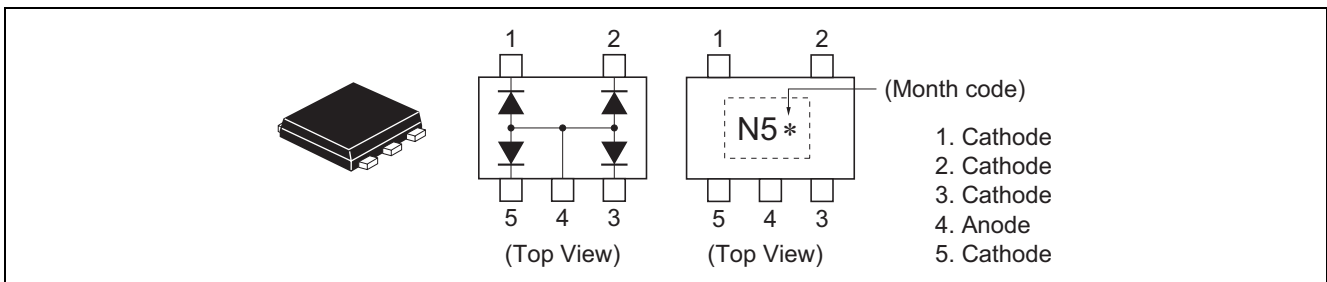
Features

- RKZ6.8Z4MFAKT has four devices in a monolithic, and can absorb surge.
- Low capacitance ($C = 4.0 \text{ pF Typ}$) and can protect ESD of signal line.
- VSON-5 Package is suitable for high density surface mounting.

Ordering Information

Type No.	Laser Mark	Package Name	Package Code
RKZ6.8Z4MFAKT	N5	VSON-5	PUSN0005ZB-A

Pin Arrangement



Month Code

Month of Manufacture	Assemble	
	JAPAN	MALAYSIA
January	A	1
February	B	2
March	C	3
April	D	4
May	E	5
June	F	6

Month of Manufacture	Assemble	
	JAPAN	MALAYSIA
July	G	7
August	H	8
September	J	9
October	K	W
November	L	X
December	M	Y

Absolute Maximum Ratings

(Ta = 25°C)

Item	Symbol	Value	Unit
Power dissipation	Pd *	150	mW
Junction temperature	Tj	150	°C
Storage temperature	Tstg	-55 to +150	°C

Note: Four device total, See Fig.2.

Electrical Characteristics *1

(Ta = 25°C)

Item	Symbol	Min	Typ	Max	Unit	Test Condition
Zener voltage	V _Z	6.47	—	7.00	V	I _Z = 5 mA, 40 ms pulse
Reverse current	I _R	—	—	2	μA	V _R = 3.5 V
Capacitance	C	—	4.0	4.5	pF	V _R = 0 V, f = 1 MHz
Dynamic resistance	r _d	—	—	30	Ω	I _Z = 5 mA
ESD-Capability *2, *3	—	8	—	—	kV	C = 150 pF, R = 330 Ω, Both forward and reverse direction 10 pulse

- Notes: 1. Per one device.
 2. Failure criterion ; I_R > 2 μA at V_R = 3.5 V.
 3. Between cathode and anode.

Main Characteristic

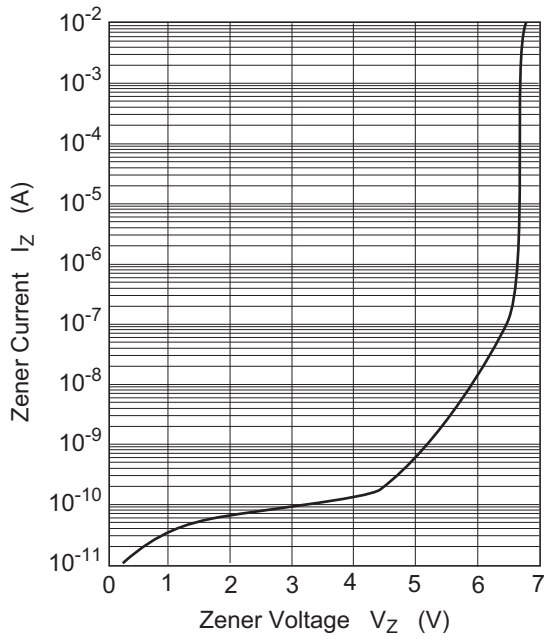


Fig.1 Zener current vs. Zener voltage

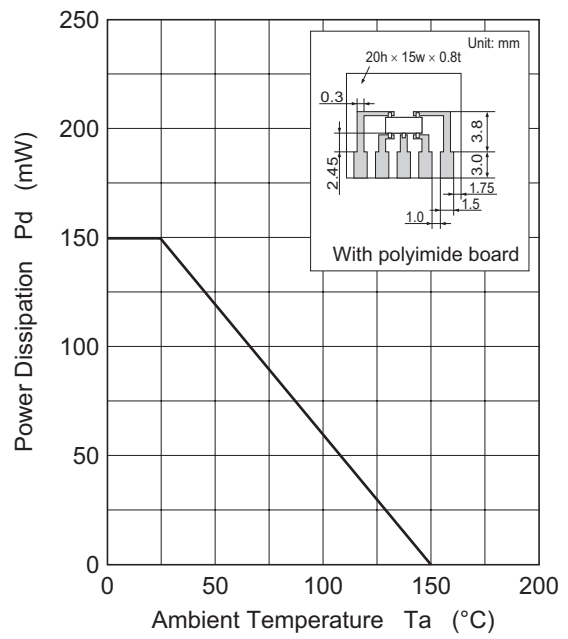


Fig.2 Power Dissipation vs. Ambient Temperature

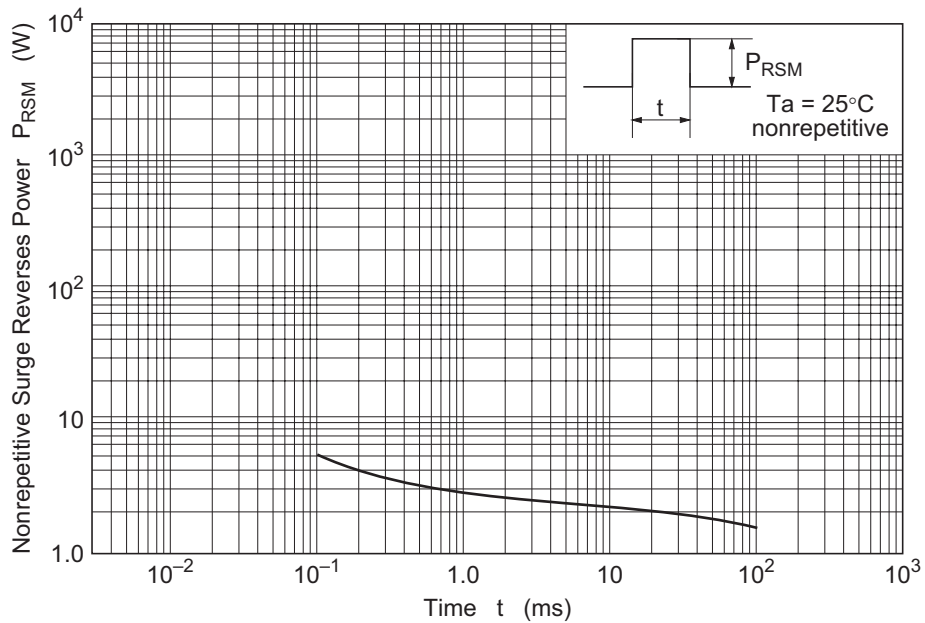
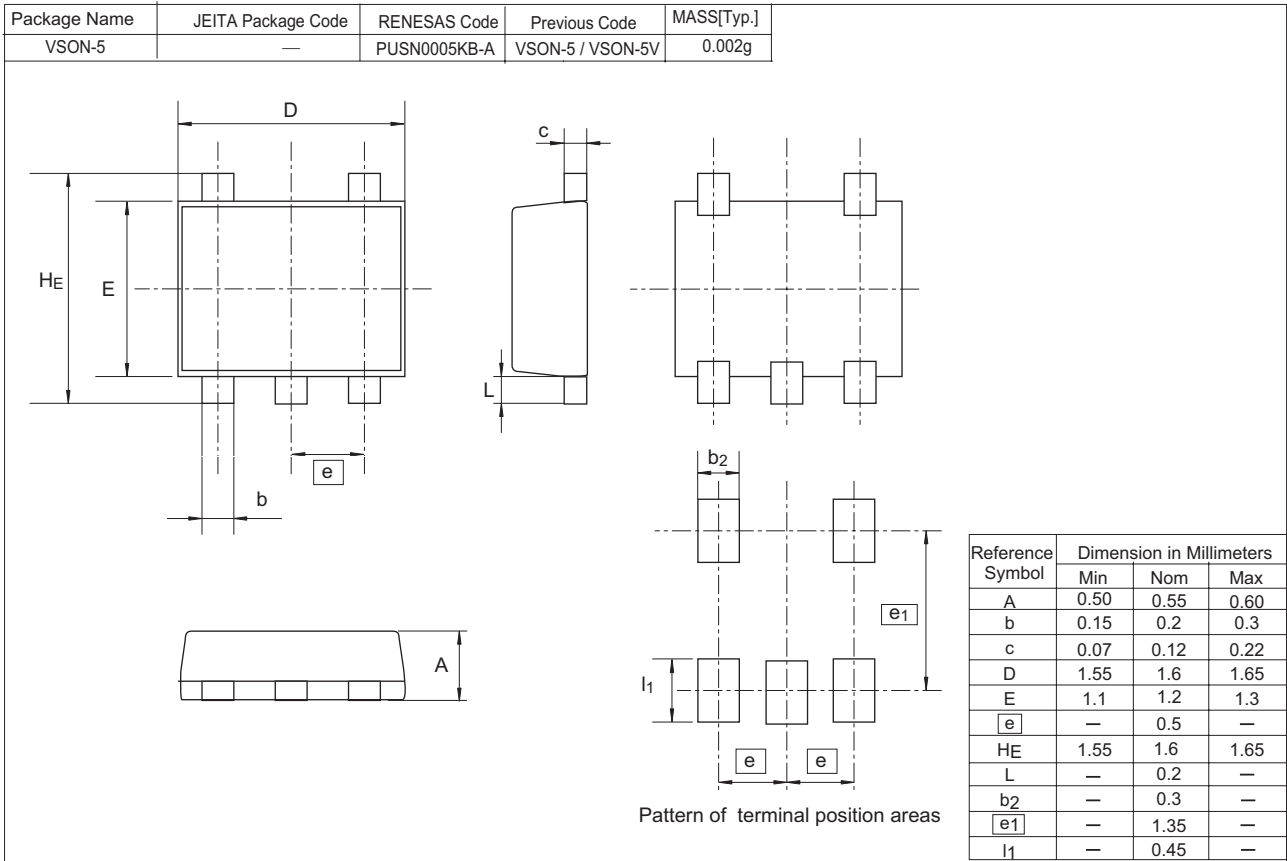


Fig.3 Surge Reverse Power Ratings

Package Dimensions



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