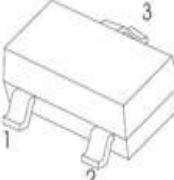
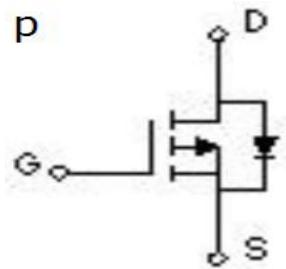
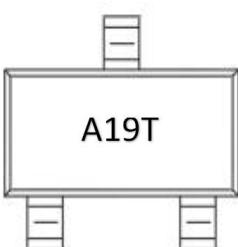


P-Channel 30-V(D-S) MOSFET	SOT-23 Plastic-Encapsulate MOSFETS
<p><u>SOT-23</u></p>  <p>1.GATE 2.SOURCE 3.DRAIN</p> <p>Equivalent Circuit</p> 	<p>Features</p> <ul style="list-style-type: none"> ※ TrenchFET Power MOSFET ※ Exceptional on-resistance and maximum DC current capability ※ High dense cell design for extremely low RDS(ON) <p>Application</p> <ul style="list-style-type: none"> ※ Load Switch for Portable Devices ※ DC/DC Converter <p>MARKING</p> 

V(BR)DSS	RDS(on)MAX	ID
-30 V	60m Ω @ -10V 65m Ω @ -4.5V 100m Ω @ -2.5V	-4.1A

Maximum ratings (Ta=25°C unless otherwise noted)

Parameter	Symbol	Value	Unit
Drain-Source Voltage	VDS	-30	V
Gate-Source Voltage	VGS	±12	
Continuous Drain Current	ID	-4.1	A
Pulsed Diode Current	IDM	-16.8	
Continuous Source-Drain Current(Diode Conduction)	IS	-1	
Power Dissipation	PD	1	W
Thermal Resistance from Junction to Ambient (t≤5s)	R _{θJA}	200	°C/W
Operating Junction	T _J	150	°C
Storage Temperature	T _{STG}	-55~+150	°C

MOSFET ELECTRICAL CHARACTERISTICS

Static Electrical Characteristics ($T_a = 25^\circ\text{C}$ Unless Otherwise Noted)

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Static						
Drain-source breakdown voltage	V(BR)DSS	$V_{GS} = 0V, ID = -250\mu\text{A}$	-30			V
Gate-source threshold voltage	$V_{GS(\text{th})}$	$V_{DS} = V_{GS}, ID = -250\mu\text{A}$	-0.7		-1.3	V
Gate-source leakage	I_{GSS}	$V_{DS} = 0V, V_{GS} = \pm 12V$			± 100	nA
Zero gate voltage drain current	I_{DSS}	$V_{DS} = -30V, V_{GS} = 0V$			1	μA
Drain-source on-state resistancea	RDS(on)	$V_{GS} = -10V, ID = -4.1\text{A}$		50	65	$\text{m}\Omega$
		$V_{GS} = -4.5V, ID = -3.6\text{A}$		60	75	$\text{m}\Omega$
		$V_{GS} = -2.5V, ID = -2\text{A}$		75	90	$\text{m}\Omega$
Forward transconductancea	g_{fs}	$V_{DS} = -4.5V, ID = -4.1\text{A}$	7			S
Diode forward voltage	V_{SD}	$IS = -1\text{A}, V_{GS} = 0V$			-1.3	V
Dynamic						
Input capacitance	C_{iss}	$V_{DS} = -15V, V_{GS} = 0V, f = 1\text{MHz}$		950		pF
Output capacitance	C_{oss}			95		pF
Reverse transfer capacitanceb	C_{rss}			77		pF
Total gate charge	Q_g	$V_{DS} = -15V, V_{GS} = -10V, ID = -4.1\text{A}$		14		nC
Gate-source charge	Q_{gs}			1.5		nC
Gate-drain charge	Q_{gd}			2.5		nC
Gate resistance	R_g	$f = 1\text{MHz}$		8		Ω
Switchingb						
Turn-on delay time	$t_{d(on)}$	$V_{DD} = -15V, RL = 4\Omega, ID \approx -1\text{A}, V_{GEN} = -10V, R_g = 3\Omega$		6.5		ns
Rise time	t_r			3.5		ns
Turn-off delay time	$t_{d(off)}$			41		ns
Fall time	t_f			9		ns
Drain-source body diode characteristics						
Continuous Source-Drain Diode Current	I_S	$T_c = 25^\circ\text{C}$			-1.3	A
Pulsed Diode forward Current	I_{SM}				-20	A

Note :

1. Repetitive Rating : Pulse width limited by maximum junction temperature.
2. Surface Mounted on FR4 Board, $t < 5$ sec.
3. Pulse Test : Pulse Width $\leq 300\mu\text{s}$, Duty Cycle $\leq 2\%$.
4. Guaranteed by design, not subject to production testing.

Typical Characteristics:

