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Vishay Siliconix

# P-Channel 40-V (D-S) MOSFET

PRODUCT SUMMARY				
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A)	Q <sub>g</sub> (TYP.)	
-40	0.0081 at V <sub>GS</sub> = -10 V	-50 <sup>d</sup>	60	
-40	0.0117 at V <sub>GS</sub> = -4.5 V	-48 <sup>d</sup>	00	



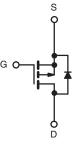
#### **FEATURES**

- TrenchFET® power MOSFET
- 100 % R<sub>g</sub> and UIS tested
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>



#### **APPLICATIONS**

- Power switch
- Load switch in high current applications
- DC/DC converters



P-Channel MOSFET

#### **Ordering Information:**

SUD50P04-08-GE3 (lead (Pb)-free and halogen-free)

<b>ABSOLUTE MAXIMUM RATINGS</b> (7	$_{\rm C}$ = 25 °C, unless other	rwise noted)		
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-Source Voltage		V <sub>DS</sub>	-40	
Gate-Source Voltage		V <sub>GS</sub>	± 20	V
Continuous Drain Current (T. 150 °C)	T <sub>C</sub> = 25 °C		-50 <sup>d</sup>	
Continuous Drain Current (T <sub>J</sub> = 150 °C)	T <sub>C</sub> = 70 °C	I <sub>D</sub>	-50 d	
Pulsed Drain Current		I <sub>DM</sub>	-100	A
Avalanche Current		I <sub>AS</sub>	-46	
Single Avalanche Energy <sup>a</sup>	L = 0.1 mH	E <sub>AS</sub>	106	mJ
Martin an Barra Biratinatina a	T <sub>C</sub> = 25 °C	В	73.5 <sup>b</sup>	W
Maximum Power Dissipation <sup>a</sup>	T <sub>A</sub> = 25 °C °C	P <sub>D</sub>	2.5	vv
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C

THERMAL RESISTANCE RATINGS			
PARAMETER	SYMBOL	LIMIT	UNIT
Junction-to-Ambient (PCB Mount) <sup>c</sup>	R <sub>thJA</sub>	50	°C/W
Junction-to-Case (Drain)	R <sub>thJC</sub>	1.7	C/VV

#### Notes

- a. Duty cycle  $\leq$  1 %.
- b. See SOA curve for voltage derating.
- c. When mounted on 1" square PCB (FR-4 material).
- d. Package limited.

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PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static						
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$	-40	-	-	V
Gate Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = -250 \mu A$	-1	-	-2.5	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$	-	-	± 250	nA
		$V_{DS} = -40 \text{ V}, V_{GS} = 0 \text{ V}$	-	-	-1	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	$V_{DS} = -40 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 125 ^{\circ}\text{C}$	-	-	-50	μΑ
		$V_{DS} = -40 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 150 ^{\circ}\text{C}$	-	-	-250	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \le -10 \text{ V}, V_{GS} = -10 \text{ V}$	-50	-	-	Α
Drain-Source On-State Resistance a	R <sub>DS(on)</sub>	$V_{GS} = -10 \text{ V}, I_D = -22 \text{ A}$	-	0.0067	0.0081	Ω
Brain Godroc Gri Gtate Nesistance	11DS(on)	$V_{GS} = -4.5 \text{ V}, I_D = -19 \text{ A}$	-	0.0097	0.0117	32
Forward Transconductance a	9 <sub>fs</sub>	$V_{DS} = -15 \text{ V}, I_{D} = -22 \text{ A}$	-	45	-	S
Dynamic <sup>b</sup>						
Input Capacitance	C <sub>iss</sub>	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = -20 V, f = 1 MHz	-	5380	-	
Output Capacitance	Coss		-	570	-	pF
Reverse Transfer Capacitance	C <sub>rss</sub>		-	500	-	
Total Gate Charge <sup>c</sup>	Qq	$V_{DS} = -20 \text{ V}$ , $V_{CS} = -10 \text{ V}$ , $I_{D} = -20 \text{ A}$	-	106	159	
Total Gate Charge	Чg		-	60	90	0
Gate-Source Charge <sup>c</sup>	$Q_{gs}$	$V_{DS} = -20 \text{ V}, V_{GS} = -4.5 \text{ V}, I_D = -20 \text{ A}$	-	22	-	nC
Gate-Drain Charge <sup>c</sup>	$Q_{gd}$		-	27	-	
Gate Resistance	$R_g$	f = 1 MHz	0.4	1.8	3.6	Ω
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>		-	15	23	
Rise Time <sup>c</sup>	t <sub>r</sub>	$V_{DD}$ = -20 V, $R_L$ = 2 $\Omega$	-	12	18	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>	$I_D \cong$ -10 A, $V_{GEN}$ = -10 V, $R_g$ = 1 $\Omega$	-	70	105	ns
Fall Time <sup>c</sup>	t <sub>f</sub>		-	18	27	
Drain-Source Body Diode Ratings at	nd Characteri	stics (T <sub>C</sub> = 25 °C) <sup>b</sup>				
Continuous Current	I <sub>S</sub>		-	-	-50	А
Pulsed Current	I <sub>SM</sub>		-	-	-100	
Forward Voltage <sup>a</sup>	V <sub>SD</sub>	$I_F = -10 \text{ A}, V_{GS} = 0 \text{ V}$	-	-0.8	-1.5	٧
Reverse Recovery Time	trr		-	35	53	ns
Peak Reverse Recovery Current	I <sub>RM(REC)</sub>	I <sub>F</sub> = -10 A, dI/dt = 100 A/μs	-	-2	-3	Α
Reverse Recovery Charge	Q <sub>rr</sub>		-	33	50	nC

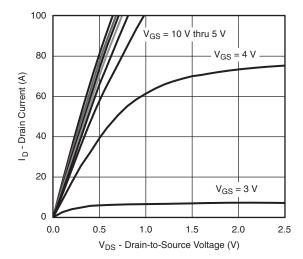
#### **Notes**

- a. Pulse test; pulse width  $\leq$  300  $\mu$ s, duty cycle  $\leq$  2 %.
- b. Guaranteed by design, not subject to production testing.
- c. Independent of operating temperature.

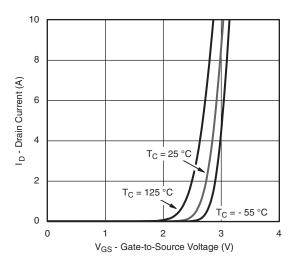
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



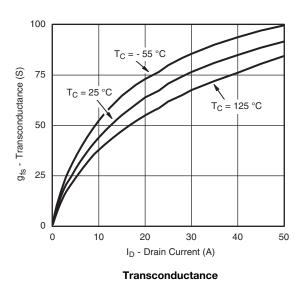
# TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

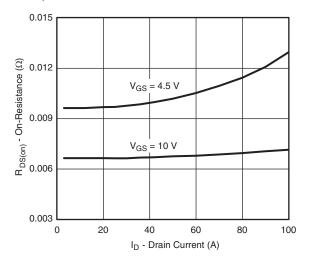


#### **Output Characteristics**

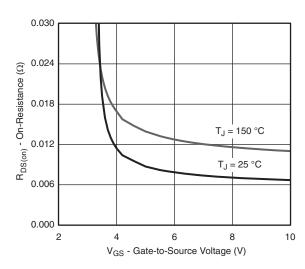


Transfer Characteristics

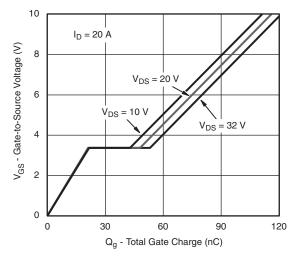




**On-Resistance vs. Drain Current** 

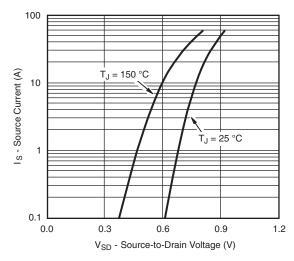


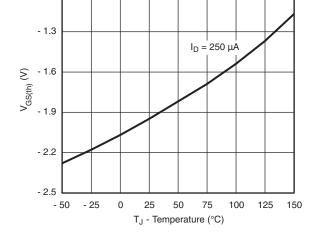
On-Resistance vs. Gate-to-Source Voltage





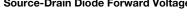
# TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

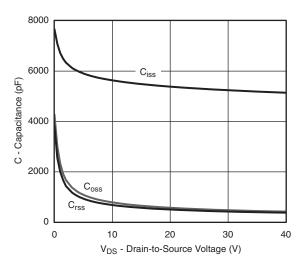




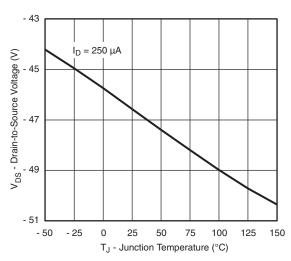
- 1.0

#### Source-Drain Diode Forward Voltage

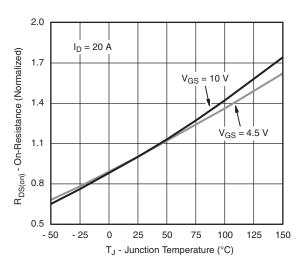




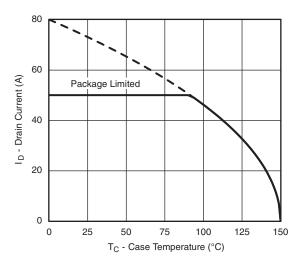
**Threshold Voltage** 



### Capacitance



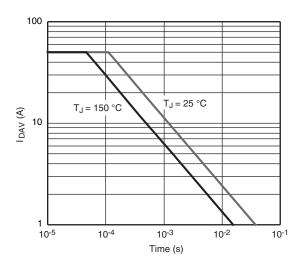
Drain Source Breakdown vs. Junction Temperature



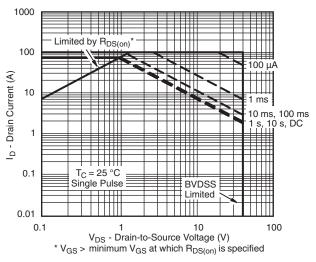
On-Resistance vs. Junction Temperature



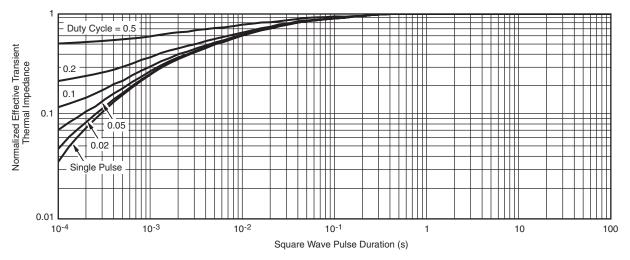
# TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Single Pulse Avalanche Current Capability vs. Time



Safe Operating Area



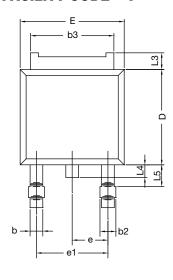
Normalized Thermal Transient Impedance, Junction-to-Case

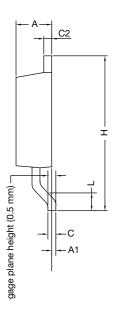
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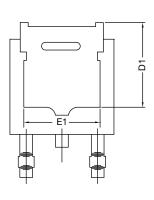


# **TO-252AA Case Outline**

## **VERSION 1: FACILITY CODE = Y**







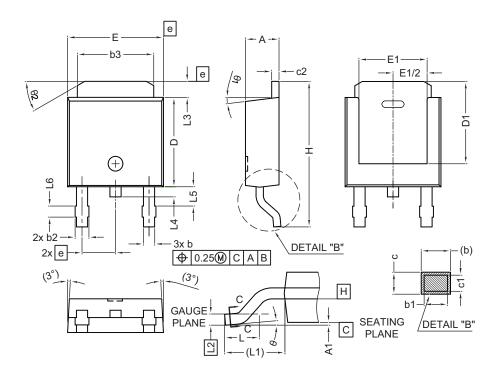
	MILLIMETERS		
DIM.	MIN.	MAX.	
Α	2.18	2.38	
A1	-	0.127	
b	0.64	0.88	
b2	0.76	1.14	
b3	4.95	5.46	
С	0.46	0.61	
C2	0.46	0.89	
D	5.97	6.22	
D1	4.10	-	
E	6.35	6.73	
E1	4.32	=	
Н	9.40	10.41	
е	2.28 BSC		
e1	4.56 BSC		
L	1.40 1.78		
L3	0.89 1.27		
L4	- 1.02		
L5	1.01	1.01 1.52	

#### Note

• Dimension L3 is for reference only



### **VERSION 2: FACILITY CODE = N**



	MILLIMETERS		
DIM.	MIN.	MAX.	
А	2.18	2.39	
A1	-	0.13	
b	0.65	0.89	
b1	0.64	0.79	
b2	0.76	1.13	
b3	4.95	5.46	
С	0.46	0.61	
c1	0.41	0.56	
c2	0.46	0.60	
D	5.97	6.22	
D1	5.21	-	
Е	6.35	6.73	
E1	4.32	-	
е	2.29 BSC		
Н	9.94	10.34	

	MILLIMETERS		
DIM.	MIN.	MAX.	
L	1.50	1.78	
L1	2.74 ref.		
L2	0.51 BSC		
L3	0.89	1.27	
L4	-	1.02	
L5	1.14	1.49	
L6	0.65	0.85	
θ	0°	10°	
θ1	0°	15°	
θ2	25°	35°	

### Notes

- Dimensioning and tolerance confirm to ASME Y14.5M-1994
- All dimensions are in millimeters. Angles are in degrees
- Heat sink side flash is max. 0.8 mm
- Radius on terminal is optional

ECN: E22-0399-Rev. R, 03-Oct-2022

DWG: 5347



# **RECOMMENDED MINIMUM PADS FOR DPAK (TO-252)**



Recommended Minimum Pads Dimensions in Inches/(mm)

Return to Index

APPLICATION NOTE



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