

MJE18004D2G

High Speed, High Gain Bipolar NPN Power Transistor

with Integrated Collector–Emitter Diode and Built–in Efficient Antisaturation Network

The MJE18004D2 is state-of-art High Speed High gain BIPolar transistor (H2BIP). High dynamic characteristics and lot to lot minimum spread (± 150 ns on storage time) make it ideally suitable for light ballast applications. Therefore, there is no need to guarantee an h_{FE} window.

It's characteristics make it also suitable for PFC application.

Features

- Low Base Drive Requirement
- High Peak DC Current Gain (55 Typical) @ $I_C = 100$ mA
- Extremely Low Storage Time Min/Max Guarantees Due to the H2BIP Structure which Minimizes the Spread
- Integrated Collector–Emitter Free Wheeling Diode
- Fully Characterized and Guaranteed Dynamic $V_{CE(sat)}$
- “6 Sigma” Process Providing Tight and Reproducible Parameter Spreads
- These Devices are Pb–Free and are RoHS Compliant*

MAXIMUM RATINGS ($T_J = 25^\circ\text{C}$ unless otherwise noted)

Rating	Symbol	Value	Unit
Collector–Emitter Sustaining Voltage	V_{CEO}	450	Vdc
Collector–Base Breakdown Voltage	V_{CBO}	1000	Vdc
Collector–Emitter Breakdown Voltage	V_{CES}	1000	Vdc
Emitter–Base Voltage	V_{EBO}	12	Vdc
Collector Current – Continuous – Peak (Note 1)	I_C I_{CM}	5 10	Adc
Base Current – Continuous – Peak (Note 1)	I_B I_{BM}	2 4	Adc
Total Device Dissipation @ $T_C = 25^\circ\text{C}$ Derate above 25°C	P_D	75 0.6	W W/ $^\circ\text{C}$
Operating and Storage Temperature	T_J, T_{stg}	–65 to 150	$^\circ\text{C}$

THERMAL CHARACTERISTICS

Thermal Resistance, Junction–to–Case	$R_{\theta JC}$	1.65	$^\circ\text{C}/\text{W}$
Thermal Resistance, Junction–to–Ambient	$R_{\theta JA}$	62.5	$^\circ\text{C}/\text{W}$
Maximum Lead Temperature for Soldering Purposes: 1/8" from Case for 5 Seconds	T_L	260	$^\circ\text{C}$

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

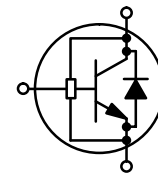
1. Pulse Test: Pulse Width = 5 ms, Duty Cycle $\leq 10\%$.



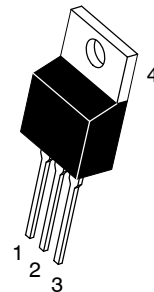
ON Semiconductor®

<http://onsemi.com>

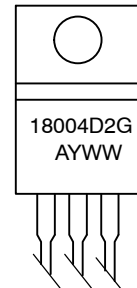
**POWER TRANSISTORS
5 AMPERES,
1000 VOLTS, 75 WATTS**



MARKING DIAGRAM



TO-220AB
CASE 221A
STYLE 1



18004D2 = Device Code
G = Pb–Free Package
A = Assembly Location
Y = Year
WW = Work Week

ORDERING INFORMATION

Device	Package	Shipping†
MJE18004D2G	TO-220AB (Pb–Free)	50 Units / Rail

*For additional information on our Pb–Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

MJE18004D2G

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit			
OFF CHARACTERISTICS								
Collector-Emitter Sustaining Voltage (I _C = 100 mA, L = 25 mH)	V _{CEO(sus)}	450	547	-	Vdc			
Collector-Base Breakdown Voltage (I _{CB0} = 1 mA)	V _{CB0}	1000	1100	-	Vdc			
Emitter-Base Breakdown Voltage (I _{EBO} = 1 mA)	V _{EBO}	12	14	-	Vdc			
Collector Cutoff Current (V _{CE} = Rated V _{CEO} , I _B = 0)	I _{CEO}	-	-	100	μAdc			
Collector Cutoff Current (V _{CE} = Rated V _{CE(sat)} , V _{EB} = 0) (V _{CE} = 500 V, V _{EB} = 0)	I _{CES}	-	-	100 500 100	μAdc			
@ T _C = 25°C @ T _C = 125°C @ T _C = 125°C								
Emitter-Cutoff Current (V _{EB} = 10 Vdc, I _C = 0)	I _{EBO}	-	-	100	μAdc			
ON CHARACTERISTICS								
Base-Emitter Saturation Voltage (I _C = 0.8 Adc, I _B = 80 mAdc)	V _{BE(sat)}	@ T _C = 25°C	-	0.8	1	Vdc		
(I _C = 2 Adc, I _B = 0.4 Adc)		@ T _C = 125°C	-	0.7	0.9			
Collector-Emitter Saturation Voltage (I _C = 0.8 Adc, I _B = 80 mAdc)	V _{CE(sat)}	@ T _C = 25°C	-	0.38	0.5	Vdc		
(I _C = 2 Adc, I _B = 0.4 Adc)		@ T _C = 125°C	-	0.55	0.75			
(I _C = 0.8 Adc, I _B = 40 mAdc)		@ T _C = 25°C	-	0.45	0.75			
(I _C = 1 Adc, I _B = 0.2 Adc)		@ T _C = 125°C	-	0.75	1			
DC Current Gain (I _C = 0.8 Adc, V _{CE} = 1 Vdc)	h _{FE}	@ T _C = 25°C	15	28	-	-		
(I _C = 2 Adc, V _{CE} = 1 Vdc)		@ T _C = 125°C	10	14	-			
(I _C = 1 Adc, V _{CE} = 2.5 Vdc)		@ T _C = 25°C	6	8	-			
		@ T _C = 125°C	4	6	-			
		@ T _C = 25°C	18	28	-			
		@ T _C = 125°C	14	20	-			
DYNAMIC SATURATION VOLTAGE								
Dynamic Saturation Voltage: Determined 1 μs and 3 μs respectively after rising I _{B1} reaches 90% of final I _{B1}	I _C = 1 Adc I _{B1} = 100 mA V _{CC} = 300 V	@ 1 μs	@ T _C = 25°C	V _{CE(dsat)}	-	9	-	V
		@ 3 μs	@ T _C = 125°C		-	16	-	
	I _C = 2 Adc I _{B1} = 0.4 A V _{CC} = 300 V	@ 1 μs	@ T _C = 25°C	-	3.1	-		
		@ 3 μs	@ T _C = 125°C	-	9	-		
		@ 1 μs	@ T _C = 25°C	-	11	-		
		@ 3 μs	@ T _C = 125°C	-	18	-		
		@ 1 μs	@ T _C = 25°C	-	1.4	-		
		@ 3 μs	@ T _C = 125°C	-	8	-		

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ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
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DIODE CHARACTERISTICS

Forward Diode Voltage (I _{EC} = 1 Adc) (I _{EC} = 2 Adc)	@ T _C = 25°C @ T _C = 125°C	V _{EC}	-	0.96 0.72	1.5	V
	@ T _C = 25°C @ T _C = 125°C		-	1.15 0.8	1.7	
Forward Recovery Time (I _F = 0.4 Adc, di/dt = 10 A/μs) (I _F = 1 Adc, di/dt = 10 A/μs) (I _F = 2 Adc, di/dt = 10 A/μs)	@ T _C = 25°C	t _{fr}	-	440	-	ns
	@ T _C = 25°C		-	335	-	
	@ T _C = 25°C		-	335	-	

DYNAMIC CHARACTERISTICS

Current Gain Bandwidth (I _C = 0.5 Adc, V _{CE} = 10 Vdc, f = 1 MHz)	f _T	-	13	-	MHz
Output Capacitance (V _{CB} = 10 Vdc, I _E = 0, f = 1 MHz)	C _{ob}	-	60	100	pF
Input Capacitance (I _C = 0.5 Adc, V _{CE} = 10 Vdc, f = 1 MHz)	C _{ib}	-	450	750	pF

SWITCHING CHARACTERISTICS: Resistive Load (D.C. ≤ 10%, Pulse Width = 40 μs)

Turn-on Time	I _C = 2.5 Adc, I _{B1} = 0.5 Adc I _{B2} = 1 Adc V _{CC} = 250 Vdc	@ T _C = 25°C	t _{on}	-	500	750	ns
Turn-off Time		@ T _C = 25°C	t _{off}	1.1	-	1.4	μs
Turn-on Time	I _C = 2 Adc, I _{B1} = 0.4 Adc I _{B2} = 1 Adc V _{CC} = 300 Vdc	@ T _C = 25°C @ T _C = 125°C	t _{on}	-	100 150	150	ns
Turn-off Time		@ T _C = 25°C @ T _C = 125°C	t _{off}	-	1.15 1.6	1.3	μs
Turn-on Time	I _C = 2.5 Adc, I _{B1} = 0.5 Adc I _{B2} = 0.5 Adc V _{CC} = 300 Vdc	@ T _C = 25°C @ T _C = 125°C	t _{on}	-	120 500	150	ns
Turn-off Time		@ T _C = 25°C @ T _C = 125°C	t _{off}	1.85	-	2.15	μs

SWITCHING CHARACTERISTICS: Inductive Load (V_{CC} = 15 V)

Fall Time	I _C = 2.5 Adc I _{B1} = 500 mA I _{B2} = 500 mA V _Z = 350 V L _C = 300 μH	@ T _C = 25°C @ T _C = 125°C	t _f	-	130 300	175	ns
Storage Time		@ T _C = 25°C @ T _C = 125°C	t _s	-	2.12 2.6	2.4	μs
Crossover Time		@ T _C = 25°C @ T _C = 125°C	t _c	-	355 750	500	ns
Fall Time	I _C = 2 Adc I _{B1} = 400 mA I _{B2} = 400 mA V _Z = 300 V L _C = 200 μH	@ T _C = 25°C @ T _C = 125°C	t _f	-	95 230	150	ns
Storage Time		@ T _C = 25°C @ T _C = 125°C	t _s	2.1	-	2.4	μs
Crossover Time		@ T _C = 25°C @ T _C = 125°C	t _c	-	300 700	450	ns
Fall Time	I _C = 1 Adc I _{B1} = 100 mA I _{B2} = 500 mA V _Z = 300 V L _C = 200 μH	@ T _C = 25°C @ T _C = 125°C	t _f	-	70 100	90	ns
Storage Time		@ T _C = 25°C @ T _C = 125°C	t _s	-	0.7 1.05	0.9	μs
Crossover Time		@ T _C = 25°C @ T _C = 125°C	t _c	-	75 160	120	ns

TYPICAL STATIC CHARACTERISTICS

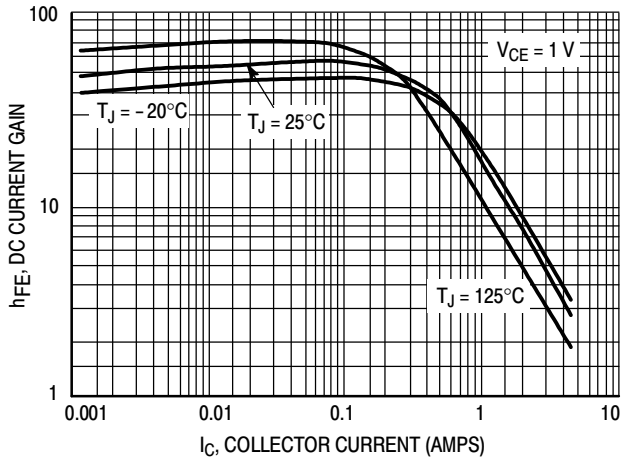


Figure 1. DC Current Gain @ 1 Volt

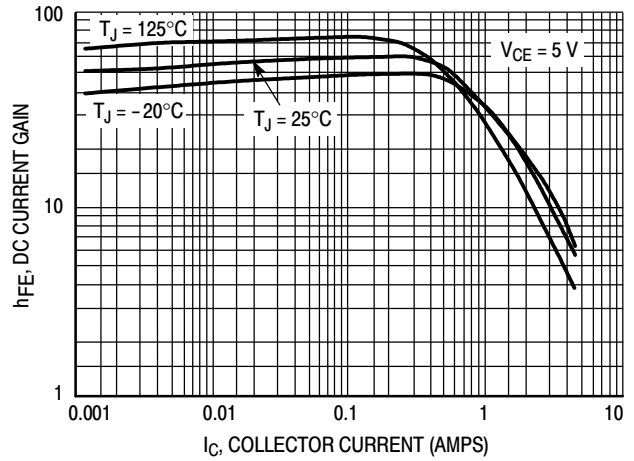


Figure 2. DC Current Gain @ 5 Volt

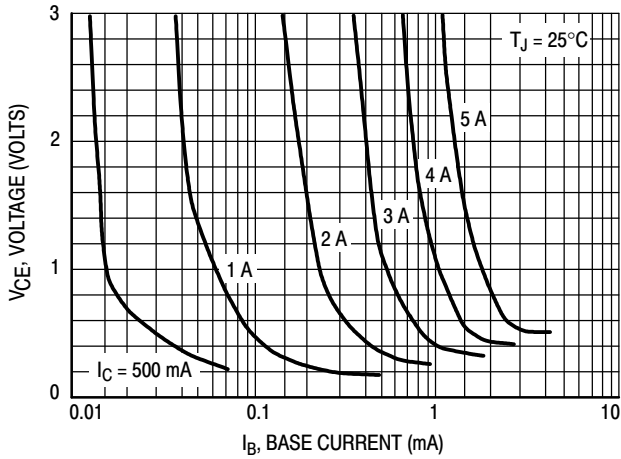


Figure 3. Collector Saturation Region

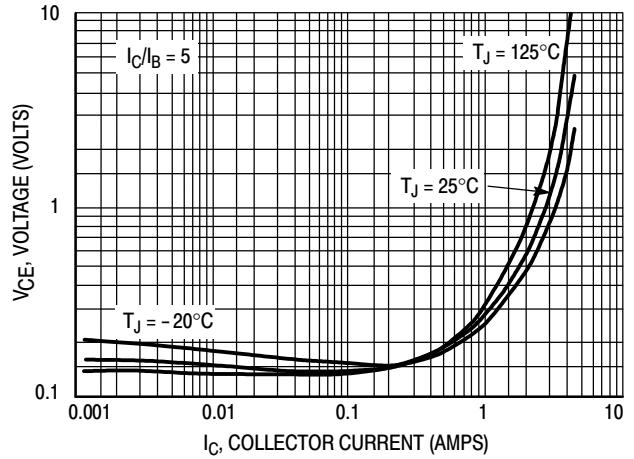


Figure 4. Collector-Emitter Saturation Voltage

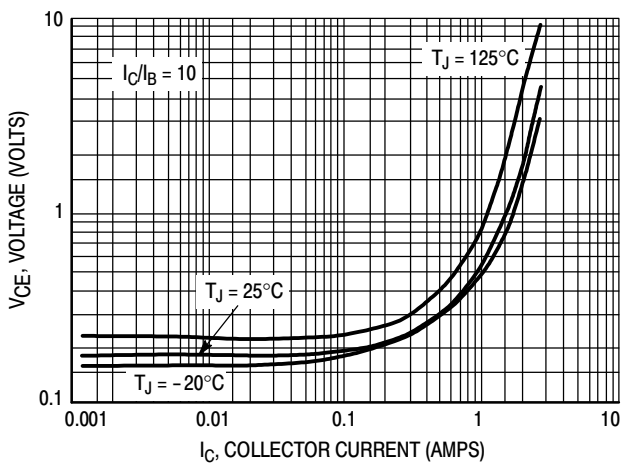


Figure 5. Collector-Emitter Saturation Voltage

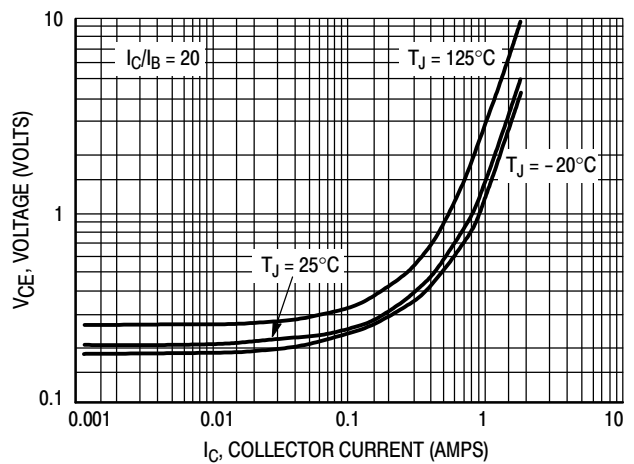


Figure 6. Collector-Emitter Saturation Voltage

TYPICAL STATIC CHARACTERISTICS

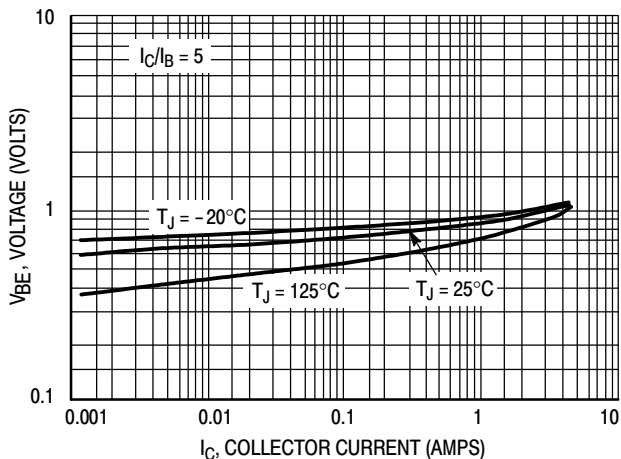


Figure 7. Base-Emitter Saturation Region

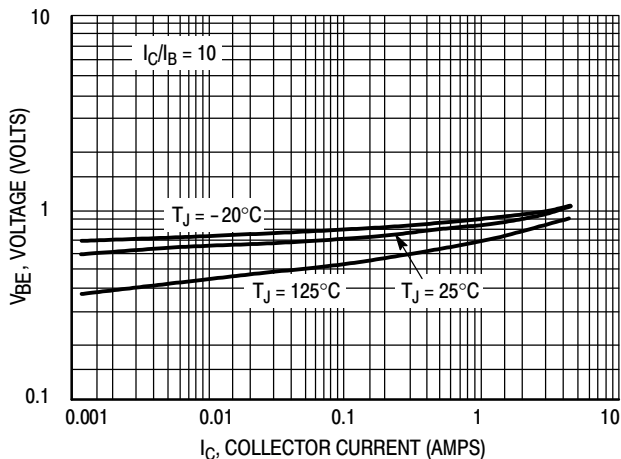


Figure 8. Base-Emitter Saturation Region

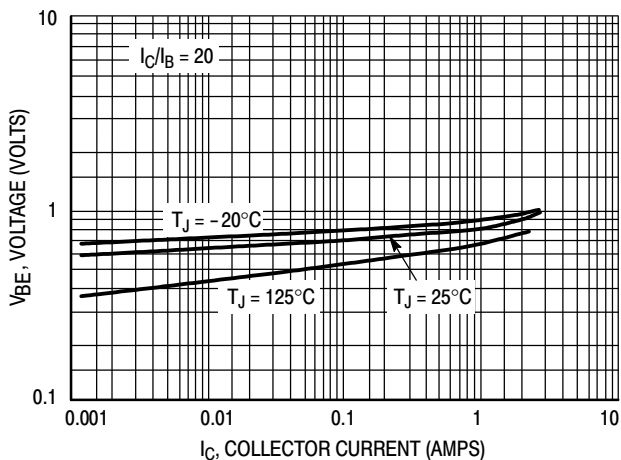


Figure 9. Base-Emitter Saturation Region

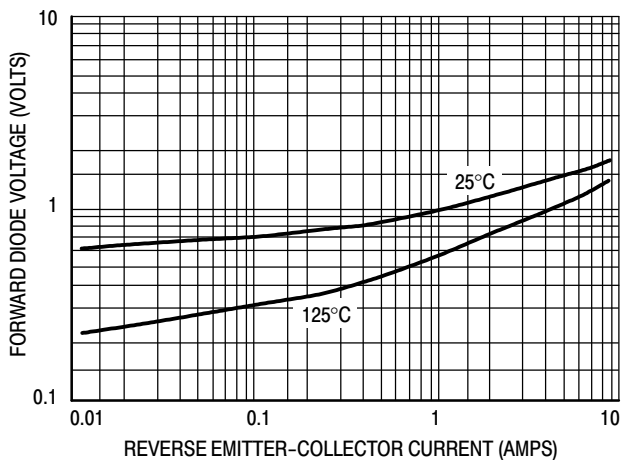


Figure 10. Forward Diode Voltage

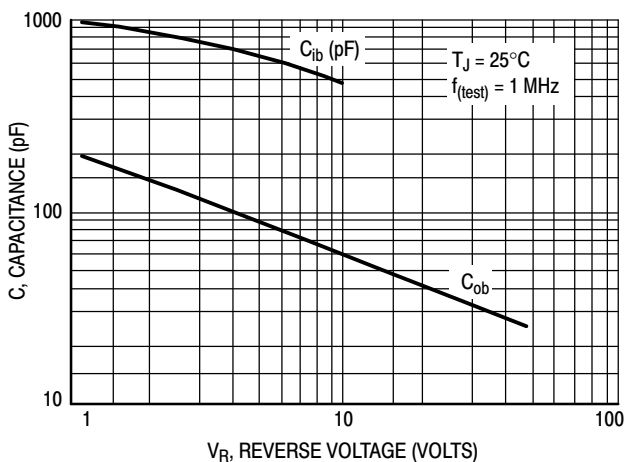


Figure 11. Capacitance

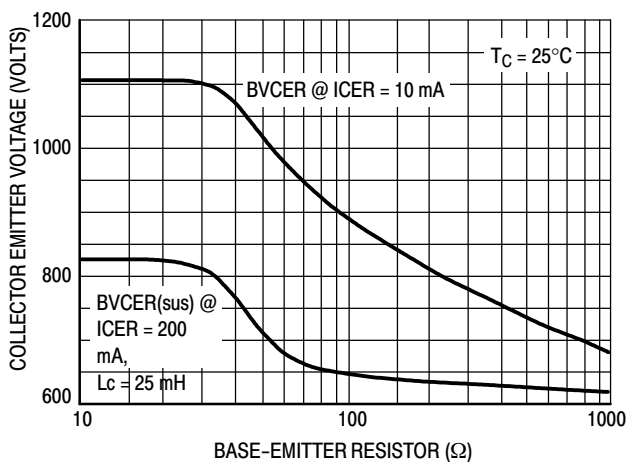


Figure 12. BVCER = f(R_{BE})

TYPICAL SWITCHING CHARACTERISTICS

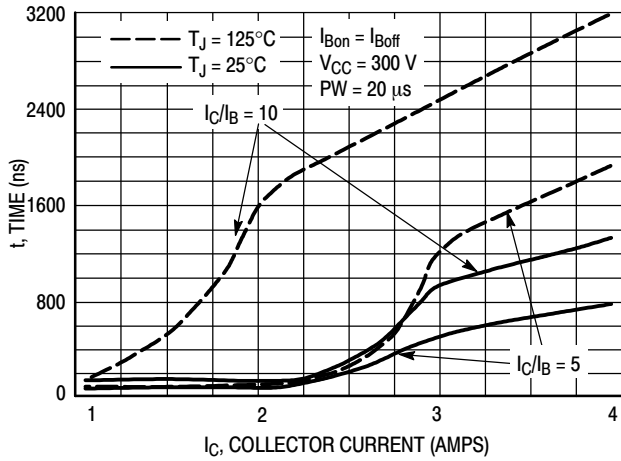


Figure 13. Resistive Switch Time, t_{on}

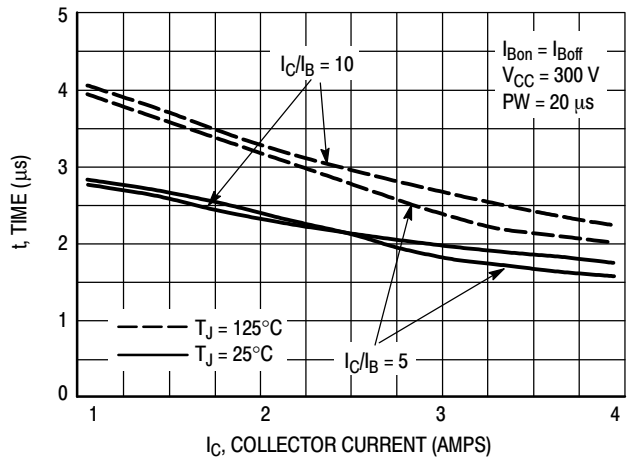


Figure 14. Resistive Switch Time, t_{off}

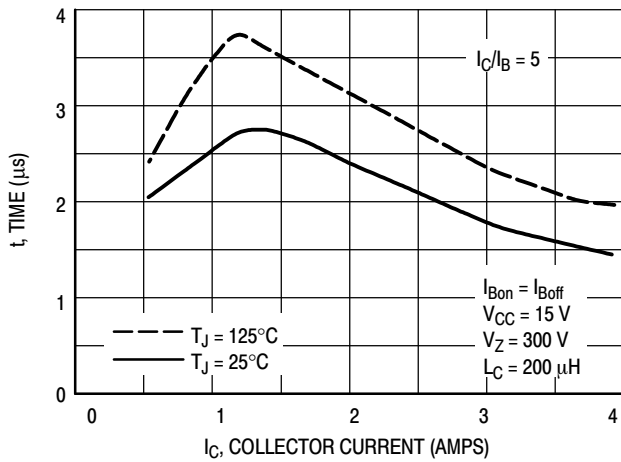


Figure 15. Inductive Storage Time, t_{si} @ $I_C/I_B = 5$

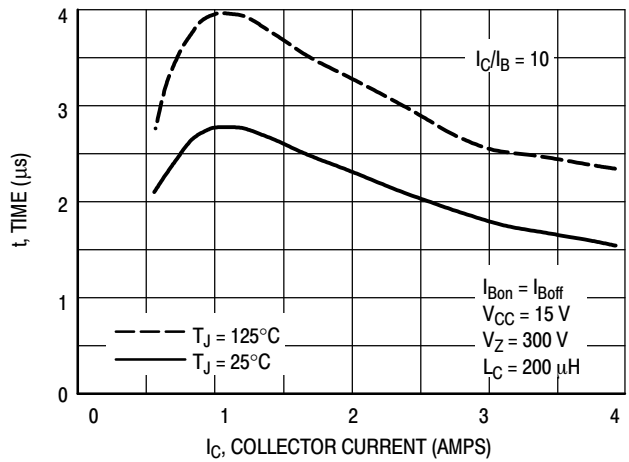


Figure 16. Inductive Storage Time, t_{si} @ $I_C/I_B = 10$

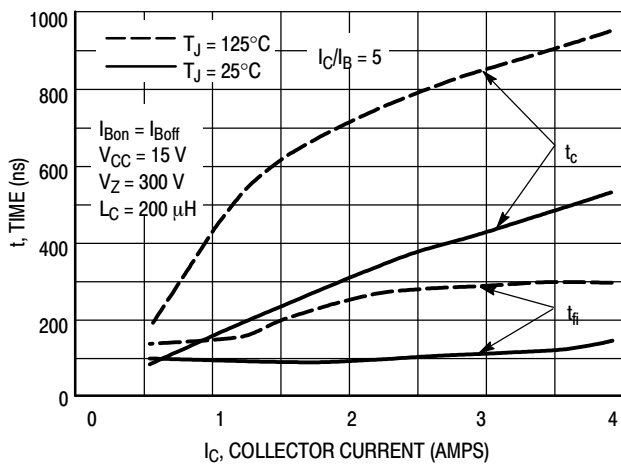


Figure 17. Inductive Switching Time, t_c and t_{fi} @ $I_C/I_B = 5$

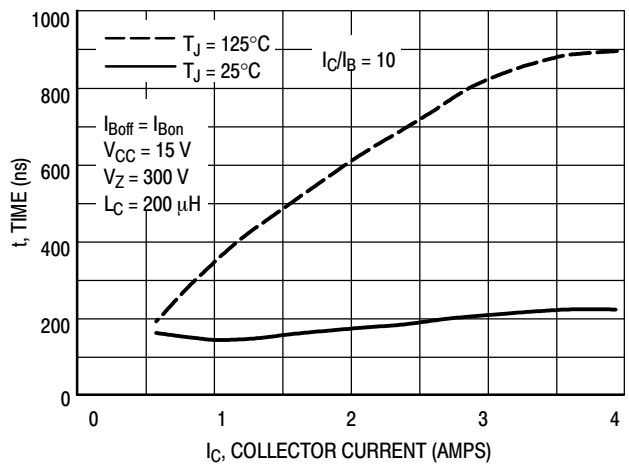


Figure 18. Inductive Switching Time, t_{fi} @ $I_C/I_B = 10$

TYPICAL SWITCHING CHARACTERISTICS

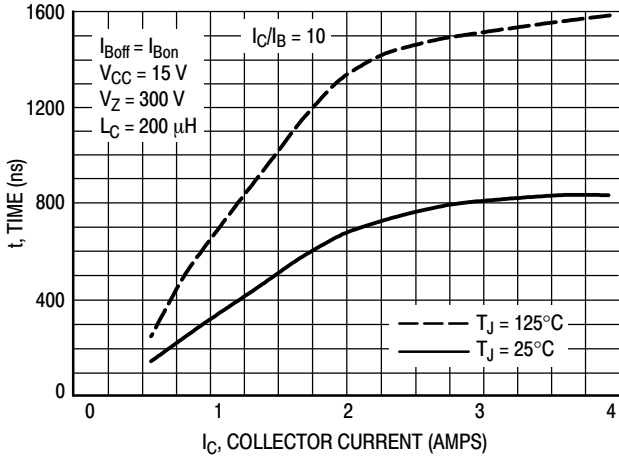


Figure 19. Inductive Switching, t_c @ $I_C/I_B = 10$

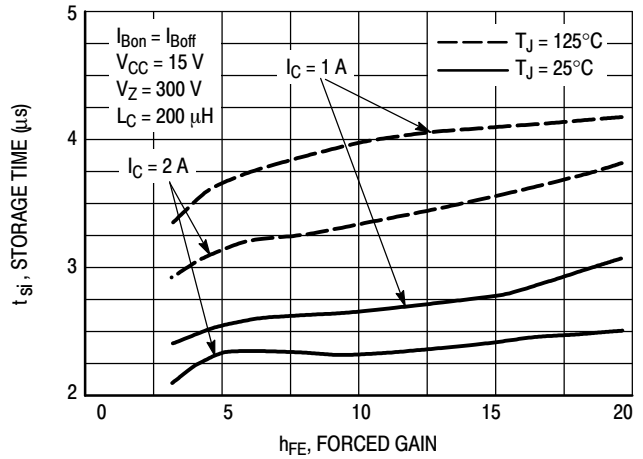


Figure 20. Inductive Storage Time

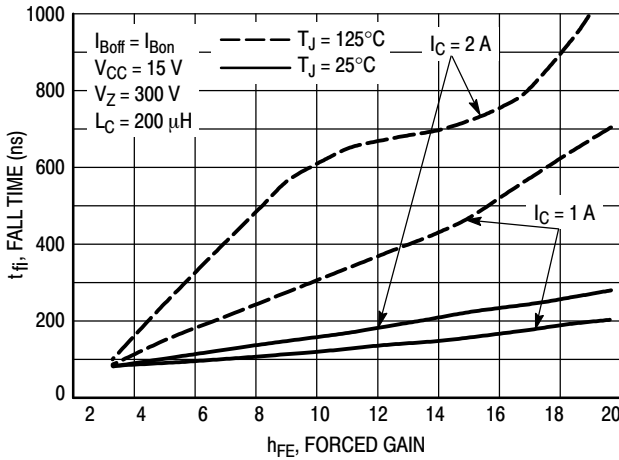


Figure 21. Inductive Fall Time

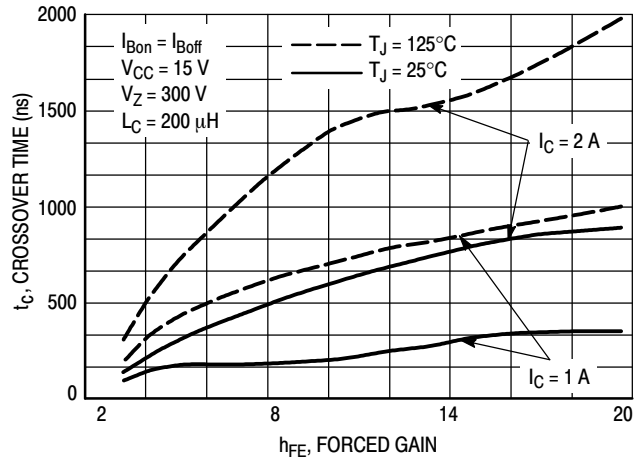


Figure 22. Inductive Crossover Time

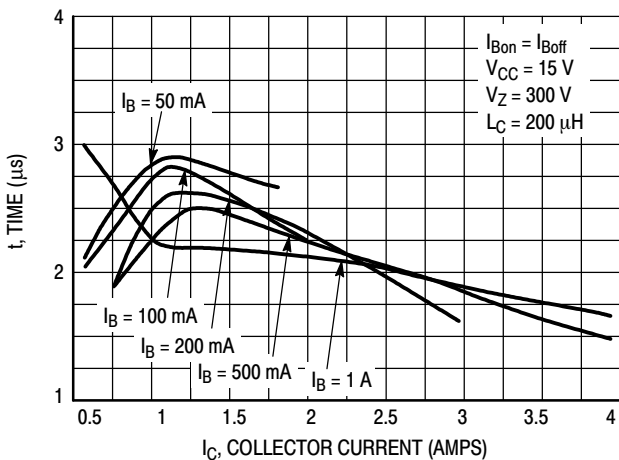


Figure 23. Inductive Storage Time, t_{si}

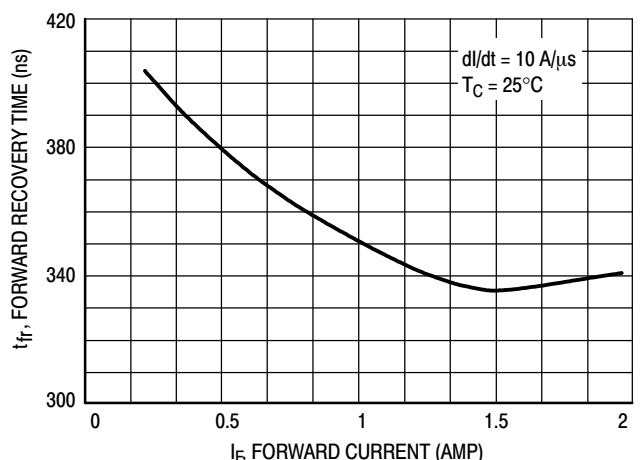


Figure 24. Forward Recovery Time, T_{FR}

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TYPICAL SWITCHING CHARACTERISTICS

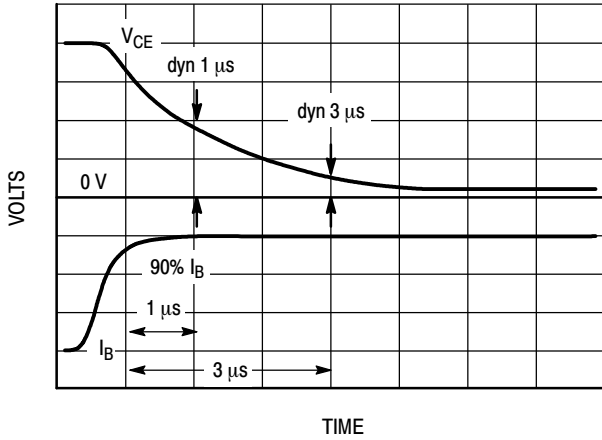


Figure 25. Dynamic Saturation Voltage Measurements

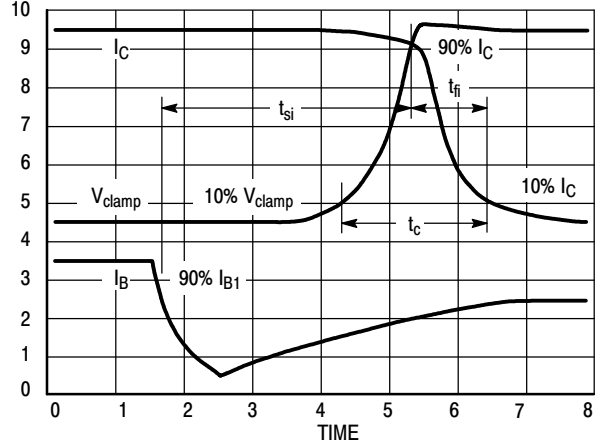


Figure 26. Inductive Switching Measurements

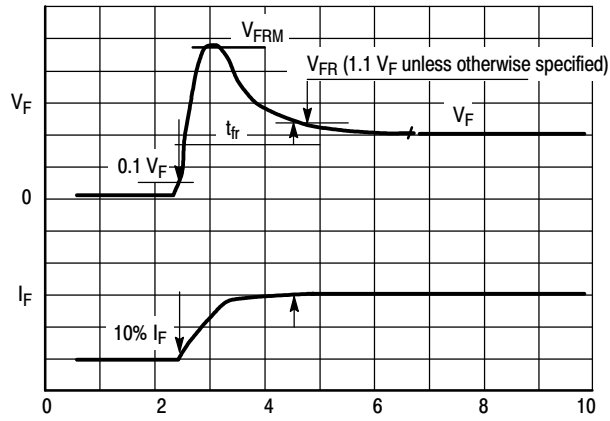
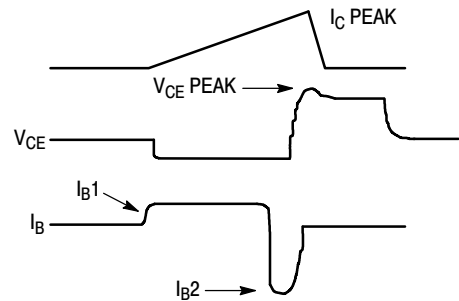
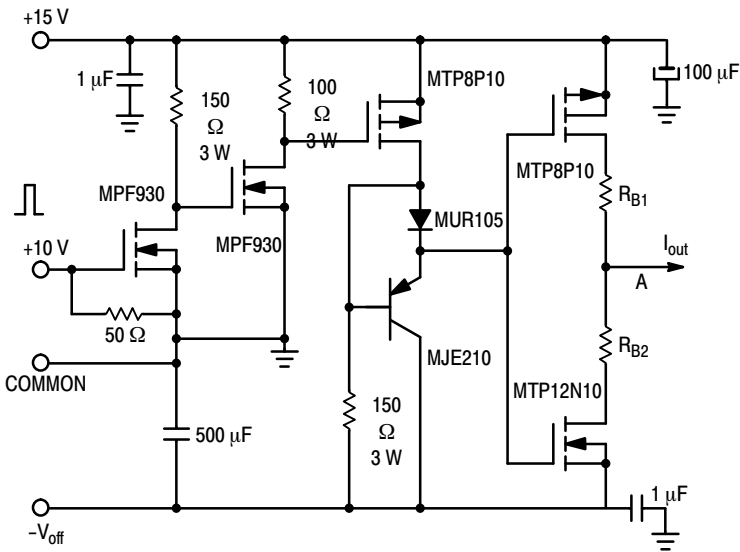


Figure 27. t_{fr} Measurements



$V_{(BR)CEO(sus)}$	Inductive Switching	RBSOA
$L = 10 \text{ mH}$	$L = 200 \text{ } \mu\text{H}$	$L = 500 \text{ } \mu\text{H}$
$RB2 = \infty$	$RB2 = 0$	$RB2 = 0$
$V_{CC} = 20 \text{ V}$	$V_{CC} = 15 \text{ V}$	$V_{CC} = 15 \text{ V}$
$I_{C(pk)} = 100 \text{ mA}$	$RB1$ selected for desired I_{B1}	$RB1$ selected for desired I_{B1}

Table 1. Inductive Load Switching Drive Circuit

TYPICAL CHARACTERISTICS

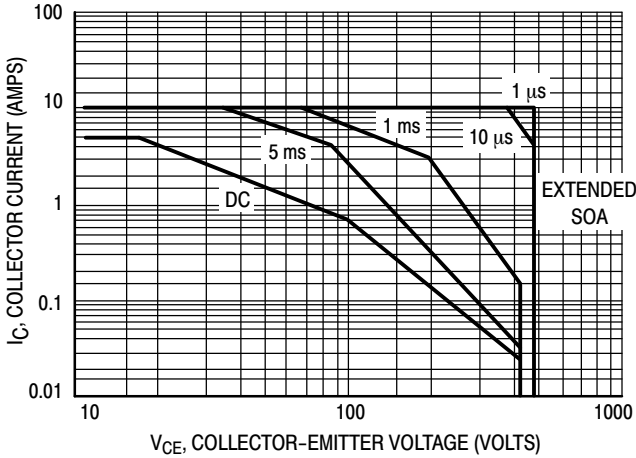


Figure 28. Forward Bias Safe Operating Area

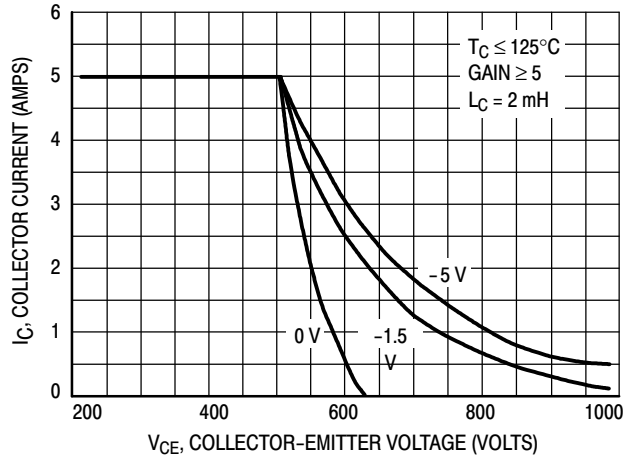


Figure 29. Reverse Bias Safe Operating Area

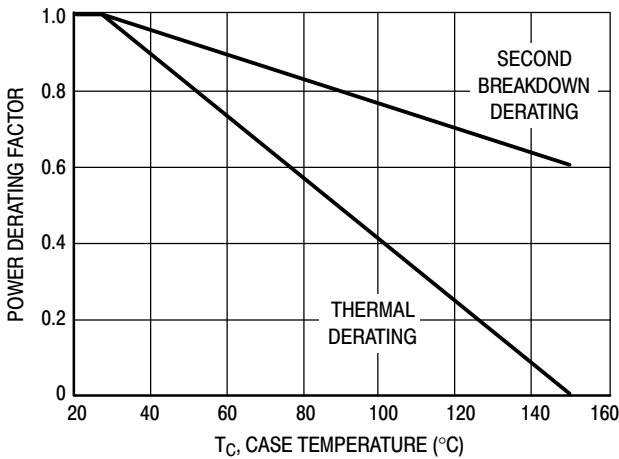


Figure 30. Forward Bias Power Derating

There are two limitations on the power handling ability of a transistor: average junction temperature and second breakdown. Safe operating area curves indicate I_C - V_{CE} limits of the transistor that must be observed for reliable operation; i.e., the transistor must not be subjected to greater dissipation than the curves indicate. The data of Figure 28 is based on $T_C = 25^\circ\text{C}$; $T_J(\text{pk})$ is variable depending on power level. Second breakdown pulse limits are valid for duty cycles to 10% but must be derated when $T_C > 25^\circ\text{C}$. Second breakdown limitations do not derate the same as thermal limitations. Allowable current at the voltages shown on Figure 28 may be found at any case temperature by using the appropriate curve on Figure 30.

$T_J(\text{pk})$ may be calculated from the data in Figure 31. At any case temperatures, thermal limitations will reduce the power that can be handled to values less than the limitations imposed by second breakdown. For inductive loads, high voltage and current must be sustained simultaneously during turn-off with the base-to-emitter junction reverse biased. The safe level is specified as a reverse-biased safe operating area (Figure 29). This rating is verified under clamped conditions so that the device is never subjected to an avalanche mode.

TYPICAL THERMAL RESPONSE

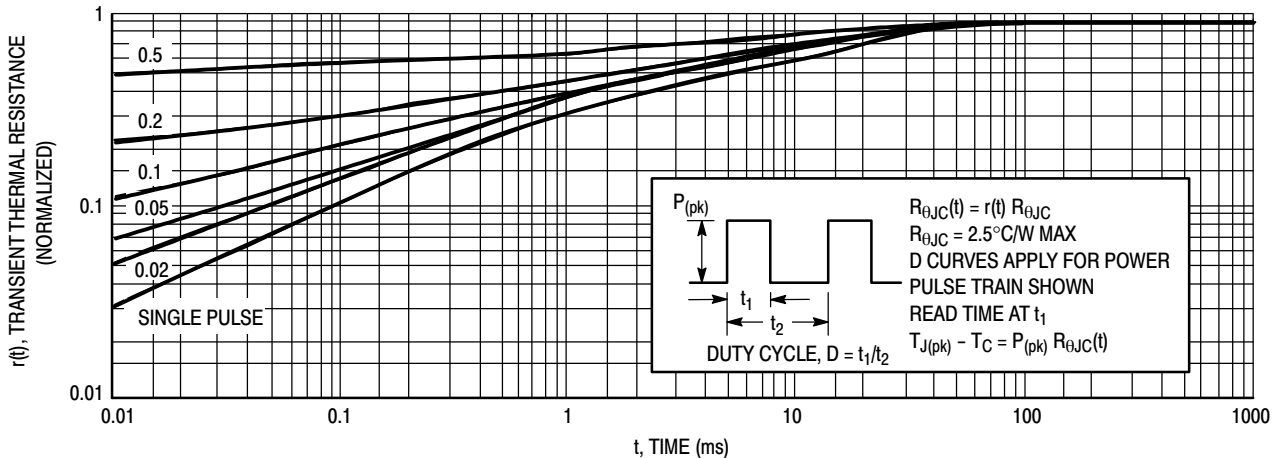
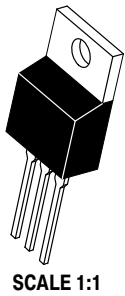


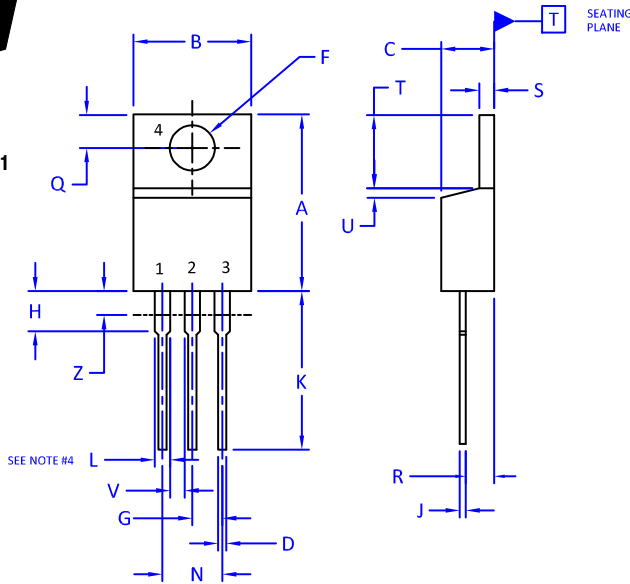
Figure 31. Typical Thermal Response ($Z_{\theta JC}(t)$) for MJE18004D2

MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS



TO-220 CASE 221A ISSUE AK

DATE 13 JAN 2022



NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 2009.
2. CONTROLLING DIMENSION: INCHES
3. DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.
4. MAX WIDTH FOR F102 DEVICE = 1.35MM

DIM	INCHES		MILLIMETERS	
	MIN.	MAX.	MIN.	MAX.
A	0.570	0.620	14.48	15.75
B	0.380	0.415	9.66	10.53
C	0.160	0.190	4.07	4.83
D	0.025	0.038	0.64	0.96
F	0.142	0.161	3.60	4.09
G	0.095	0.105	2.42	2.66
H	0.110	0.161	2.80	4.10
J	0.014	0.024	0.36	0.61
K	0.500	0.562	12.70	14.27
L	0.045	0.060	1.15	1.52
N	0.190	0.210	4.83	5.33
Q	0.100	0.120	2.54	3.04
R	0.080	0.110	2.04	2.79
S	0.045	0.055	1.15	1.41
T	0.235	0.255	5.97	6.47
U	0.000	0.050	0.00	1.27
V	0.045	---	1.15	---
Z	---	0.080	---	2.04

STYLE 1:

- PIN 1. BASE
- 2. COLLECTOR
- 3. EMITTER
- 4. COLLECTOR

STYLE 2:

- PIN 1. BASE
- 2. EMITTER
- 3. COLLECTOR
- 4. EMITTER

STYLE 3:

- PIN 1. CATHODE
- 2. ANODE
- 3. GATE
- 4. ANODE

STYLE 4:

- PIN 1. MAIN TERMINAL 1
- 2. MAIN TERMINAL 2
- 3. GATE
- 4. MAIN TERMINAL 2

STYLE 5:

- PIN 1. GATE
- 2. DRAIN
- 3. SOURCE
- 4. DRAIN

STYLE 6:

- PIN 1. ANODE
- 2. CATHODE
- 3. ANODE
- 4. CATHODE

STYLE 7:

- PIN 1. CATHODE
- 2. ANODE
- 3. CATHODE
- 4. ANODE

STYLE 8:

- PIN 1. CATHODE
- 2. ANODE
- 3. EXTERNAL TRIP/DELAY
- 4. ANODE

STYLE 9:

- PIN 1. GATE
- 2. COLLECTOR
- 3. EMITTER
- 4. COLLECTOR

STYLE 10:

- PIN 1. GATE
- 2. SOURCE
- 3. DRAIN
- 4. SOURCE

STYLE 11:

- PIN 1. DRAIN
- 2. SOURCE
- 3. GATE
- 4. SOURCE

STYLE 12:

- PIN 1. MAIN TERMINAL 1
- 2. MAIN TERMINAL 2
- 3. GATE
- 4. NOT CONNECTED

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