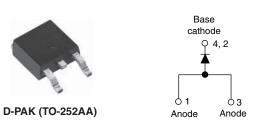
Vishay Semiconductors

High Performance Schottky Rectifier, 5.5 A

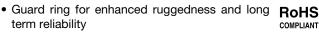


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PRODUCT SUMMARY					
Package	D-PAK (TO-252AA)				
I _{F(AV)}	5.5 A				
V _R	30 V				
V _F at I _F	See Electrical table				
I _{RM}	58 mA at 125 °C				
T _J max.	150 °C				
Diode variation	Single die				
E _{AS}	10 mJ				

FEATURES

• Low forward voltage drop



- Popular D-PAK outline
- Small foot print, surface mountable
- High frequency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 $^{\circ}\mathrm{C}$
- AEC-Q101 qualified, meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see <u>www.vishav.com/doc?99912</u>

DESCRIPTION

The VS-50WQ03FNHM3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and small foot prints on PC board. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS							
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES UNI					
I _{F(AV)}	Rectangular waveform	5.5	А				
V _{RRM}		30	V				
I _{FSM}	t _p = 5 μs sine	320	А				
V _F	5 A _{pk} , T _J = 125 °C	0.35	V				
TJ	Range	-40 to +150	°C				

VOLTAGE RATINGS			
PARAMETER	SYMBOL	VS-50WQ03FNHM3	UNITS
Maximum DC reverse voltage	V _R	30	V
Maximum working peak reverse voltage	V _{RWM}	30	v

ABSOLUTE MAXIMUM RATINGS						
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS	
Maximum average forward current See fig. 5	I _{F(AV)}	50 % duty cycle at $T_C = 136$ °C	, rectangular waveform	5.5	А	
Maximum peak one cycle		5 µs sine or 3 µs rect. pulse	Following any rated load condition and with	320	А	
non-repetitive surge current I _{FSM} See fig. 7		10 ms sine or 6 ms rect. pulse	rated V _{RRM} applied	130		
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 2 A, L = 5 mH		10	mJ	
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T _J maximum V _A = 1.5 x V _R typical		2.0	А	

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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST	CONDITIONS	VALUES	UNITS	
		5 A	T ₁ = 25 °C	0.46	v	
Maximum forward voltage drop	V (1)	10 A	1j=25 C	0.53		
See fig. 1	V _{FM} ⁽¹⁾	5 A	T 105 %C	0.35		
		10 A	— T _J = 125 °C	0.46		
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C	V Deted V	3	mA	
See fig. 2	IRM \''	T _J = 125 °C	$ V_R = Rated V_R$	58		
Threshold voltage	V _{F(TO)}	$T_{\rm J} = T_{\rm J} \text{ maximum} \qquad \qquad$		0.19	V	
Forward slope resistance	r _t			mΩ		
Typical junction capacitance	CT	$V_{R} = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz), 25 °C 590			pF	
Typical series inductance	L _S	Measured lead to lead 5 mm from package body 5.0			nH	

Note

 $^{(1)}\,$ Pulse width < 300 $\mu s,$ duty cycle < 2 $\,\%$

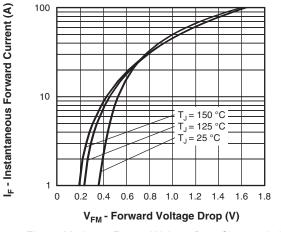
THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		-40 to +150	°C		
Maximum thermal resistance, junction to case	R _{thJC}	DC operation See fig. 4	3.0	°C/W		
Approximate weight			0.3	g		
			0.01	oz.		
Marking device		Case style D-PAK (similar to TO-252AA)	50WQ0	D3FNH		

Note

(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

VS-50WQ03FNHM3

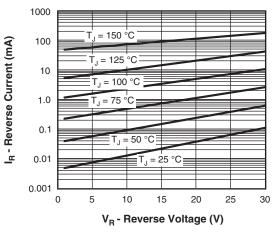
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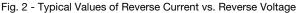


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Fig. 1 - Maximum Forward Voltage Drop Characteristics





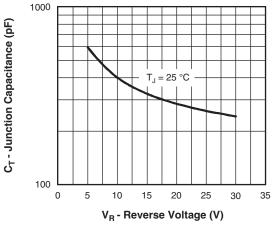


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

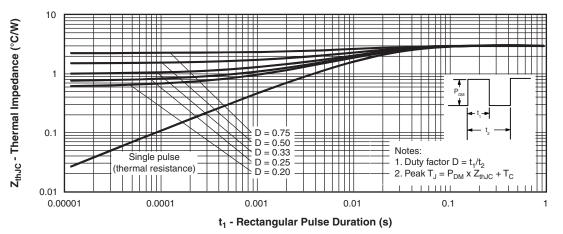
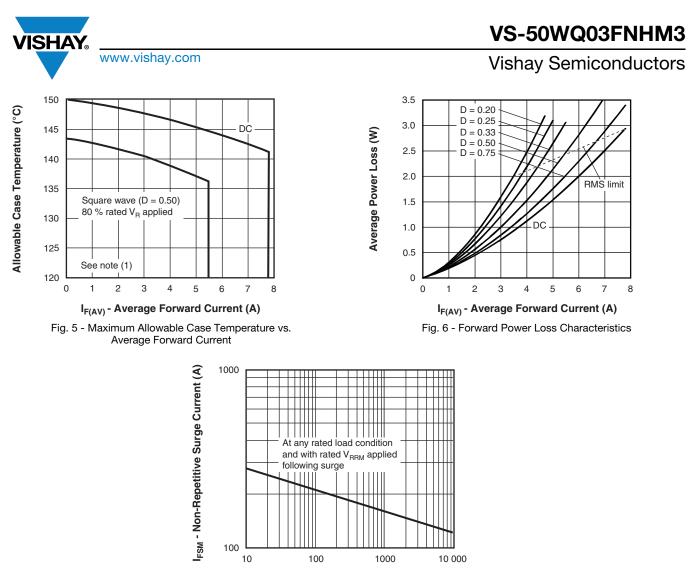


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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Note

 $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \, x \ \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see fig. 6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \, x \ \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$

⁽¹⁾ Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$;

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ORDERING INFORMATION TABLE

Device code	vs-	50	w	Q	03	FN	TRL	н	М3
		(2)	(3)	(4)	(5)	(6)	(7)	(8)	(9)
	\bigcirc		U	\bigcirc	J	U	\cdot	U	U
	1	- Visl	nay Sen	niconduc	ctors pro	oduct			
	2	- Cur	rent rati	ng (5.5 /	A)				
	3	- Pac	kage id	entifier:					
		VV =	D-PAK	<u> </u>					
	4	- Sch	ottky "C)" series					
	5	- Volt	age rati	ng (03 =	= 30 V)				
	6	- FN	= TO-2	52AA (D	-PAK)				
	7	• N	one = tu	lbe					
		• TI	R = tape	e and ree	el				
		• TF	RL = tap	e and re	el (left	orienteo	d)		
	_	• TF	RR = tap	be and re	eel (righ	t orient	ed)		
	끔	- H=	AEC-Q	101 qua	lified				
	9	- Env	vironmer	ntal digit	:				
		M3	= halog	en-free,	RoHS-0	complia	nt and t	erminati	ons lea

ORDERING INFORMATION (Example)							
PREFERRED P/N	QUANTITY PER T/R	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION				
VS-50WQ03FNHM3	75	3000	Antistatic plastic tube				
VS-50WQ03FNTRHM3	2000	2000	13" diameter reel				
VS-50WQ03FNTRLHM3	3000	3000	13" diameter reel				
VS-50WQ03FNTRRHM3	3000	3000	13" diameter reel				

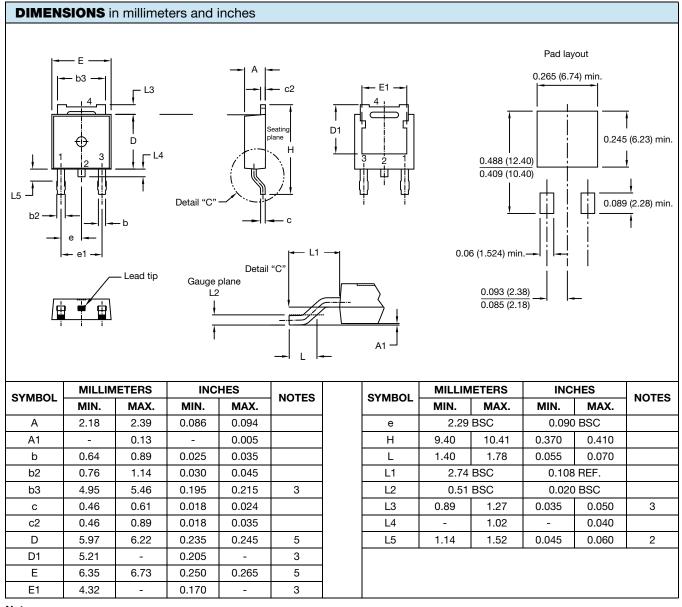
LINKS TO RELATED DOCUMENTS					
Dimensions	www.vishay.com/doc?95519				
Part marking information	www.vishay.com/doc?95518				
Packaging information	www.vishay.com/doc?95033				

Outline Dimensions



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DPAK (TO-252AA)



Notes

⁽¹⁾ Dimensioning and tolerancing as per ASME Y14.5M-1994

⁽²⁾ Lead dimension uncontrolled in L5

⁽³⁾ Dimension D1, E1, L3 and b3 establish a minimum mounting surface for thermal pad

⁽⁴⁾ Dimensions D and E do not include mold flash. Mold flash shall not exceed 0.127 mm (0.005") per side. These dimensions are measured at the outermost extremes of the plastic body

⁽⁵⁾ Outline conforms to JEDEC[®] outline TO-252AA



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