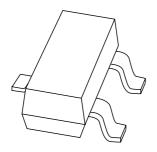
DISCRETE SEMICONDUCTORS

DATA SHEET



BSR15; **BSR16**PNP switching transistors

Product data sheet Supersedes data of 1999 Apr 15 2004 Jan 13



PNP switching transistors

BSR15; **BSR16**

FEATURES

- High current (max. 600 mA)
- Low voltage (max. 60 V).

APPLICATIONS

· Medium power switching.

DESCRIPTION

PNP switching transistor in a SOT23 plastic package. NPN complements: BSR13 and BSR14.

MARKING

TYPE NUMBER	MARKING CODE ⁽¹⁾
BSR15	T7*
BSR16	T8*

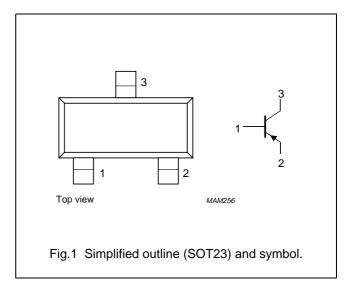
Note

1. * = p: Made in Hong Kong.

* = t : Made in Malaysia. * = W : Made in China.

PINNING

PIN	DESCRIPTION
1	base
2	emitter
3	collector



ORDERING INFORMATION

TYPE	PACKAGE		
NUMBER	NAME	DESCRIPTION	VERSION
BSR15	_	plastic surface mounted package; 3 leads	SOT23
BSR16			

PNP switching transistors

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LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	_	-60	V
V_{CEO}	collector-emitter voltage	open base			
	BSR15		_	-40	V
	BSR16		_	-60	V
V _{EBO}	emitter-base voltage	open collector	_	-5	V
Ic	collector current (DC)		_	-600	mA
I _{CM}	peak collector current		_	-800	mA
I _{BM}	peak base current		_	-200	mA
P _{tot}	total power dissipation	T _{amb} ≤ 25 °C	_	250	mW
T _{stg}	storage temperature		-65	+150	°C
T _j	junction temperature		_	150	°C
T _{amb}	operating ambient temperature		-65	+150	°C

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R _{th(j-a)}	thermal resistance from junction to ambient	note 1	500	K/W

Note

1. Transistor mounted on an FR4 printed-circuit board.

PNP switching transistors

BSR15; BSR16

CHARACTERISTICS

 $T_j = 25$ °C unless otherwise specified.

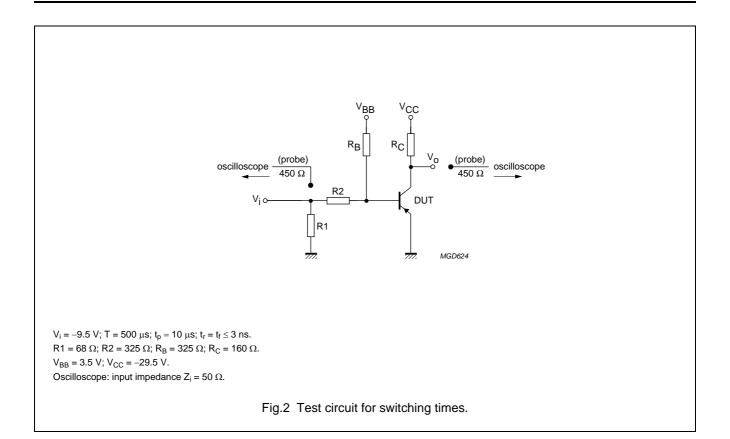
SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I _{CBO}	collector cut-off current				
	BSR15	$I_E = 0$; $V_{CB} = -50 \text{ V}$	_	-20	nA
		$I_E = 0$; $V_{CB} = -50 \text{ V}$; $T_j = 150 ^{\circ}\text{C}$	_	-20	μΑ
	collector cut-off current				
	BSR16	$I_E = 0$; $V_{CB} = -50 \text{ V}$	_	-10	nA
		$I_E = 0$; $V_{CB} = -50 \text{ V}$; $T_j = 150 ^{\circ}\text{C}$	_	-10	μΑ
I _{EBO}	emitter cut-off current	$I_C = 0$; $V_{EB} = -5 \text{ V}$	_	-50	nA
h _{FE}	DC current gain	$I_C = -0.1 \text{ mA}; V_{CE} = -10 \text{ V}$			
	BSR15		35	_	
	BSR16		75	_	
	DC current gain	$I_C = -1 \text{ mA}; V_{CE} = -10 \text{ V}$			
	BSR15		50	_	
	BSR16		100	_	
	DC current gain	$I_C = -10 \text{ mA}; V_{CE} = -10 \text{ V}$			
	BSR15		75	_	
	BSR16		100	_	
	DC current gain	$I_C = -150 \text{ mA}$; $V_{CE} = -10 \text{ V}$; note 1	100	300	
	DC current gain	$I_C = -500 \text{ mA}; V_{CE} = -10 \text{ V}; \text{ note 1}$			
	BSR15		30	_	
	BSR16		50	_	
V _{CEsat}	collector-emitter saturation	$I_C = -150 \text{ mA}; I_B = -15 \text{ mA}$	_	-400	mV
5 - 5 - 5 - 5 - 5 - 5 - 5 - 5 - 5 - 5 -	voltage	$I_C = -500 \text{ mA}$; $I_B = -50 \text{ mA}$	_	-1.6	V
V _{BEsat}	base-emitter saturation voltage	$I_C = -150 \text{ mA}; I_B = -15 \text{ mA}$	_	-1.3	V
		$I_C = -500 \text{ mA}; I_B = -50 \text{ mA}$	_	-2.6	V
C _c	collector capacitance	$I_E = i_e = 0$; $V_{CB} = -10 \text{ V}$; $f = 1 \text{ MHz}$	_	8	pF
C _e	emitter capacitance	$I_C = I_c = 0$; $V_{EB} = -2$ V; $f = 1$ MHz	_	30	pF
f _T	transition frequency	$I_C = -50 \text{ mA}$; $V_{CE} = -20 \text{ V}$; $f = 100 \text{ MHz}$	200	_	MHz
Switching t	imes (between 10% and 90% leve	els); (see Fig.2)			
t _{on}	turn-on time	I _{Con} = -150 mA; I _{Bon} = -15 mA;	_	40	ns
t _d	delay time	I _{Boff} = 15 mA	_	12	ns
t _r	rise time		_	30	ns
t _{off}	turn-off time		_	365	ns
t _s	storage time		_	300	ns
t _f	fall time		_	65	ns

Note

1. Pulse test: $t_p \leq 300~\mu s;~\delta \leq 0.02.$

PNP switching transistors

BSR15; BSR16



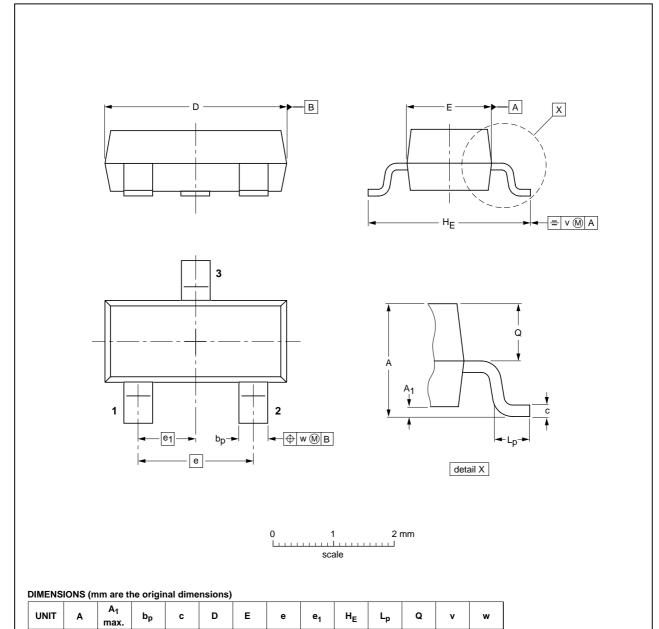
PNP switching transistors

BSR15; BSR16

PACKAGE OUTLINE



SOT23



OUTLINE	REFERENCES			EUROPEAN	ICCUE DATE	
VERSION	IEC	JEDEC	JEITA		PROJECTION	ISSUE DATE
SOT23		TO-236AB				-04-11-04 06-03-16

1.9

0.45

0.55

0.1

2004 Jan 13 6

0.48

0.38

0.9

PNP switching transistors

BSR15; **BSR16**

DATA SHEET STATUS

DOCUMENT STATUS ⁽¹⁾	PRODUCT STATUS ⁽²⁾	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

Notes

- 1. Please consult the most recently issued document before initiating or completing a design.
- The product status of device(s) described in this document may have changed since this document was published
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Customer notification

This data sheet was changed to reflect the new company name NXP Semiconductors, including new legal definitions and disclaimers. No changes were made to the technical content, except for package outline drawings which were updated to the latest version.

Contact information

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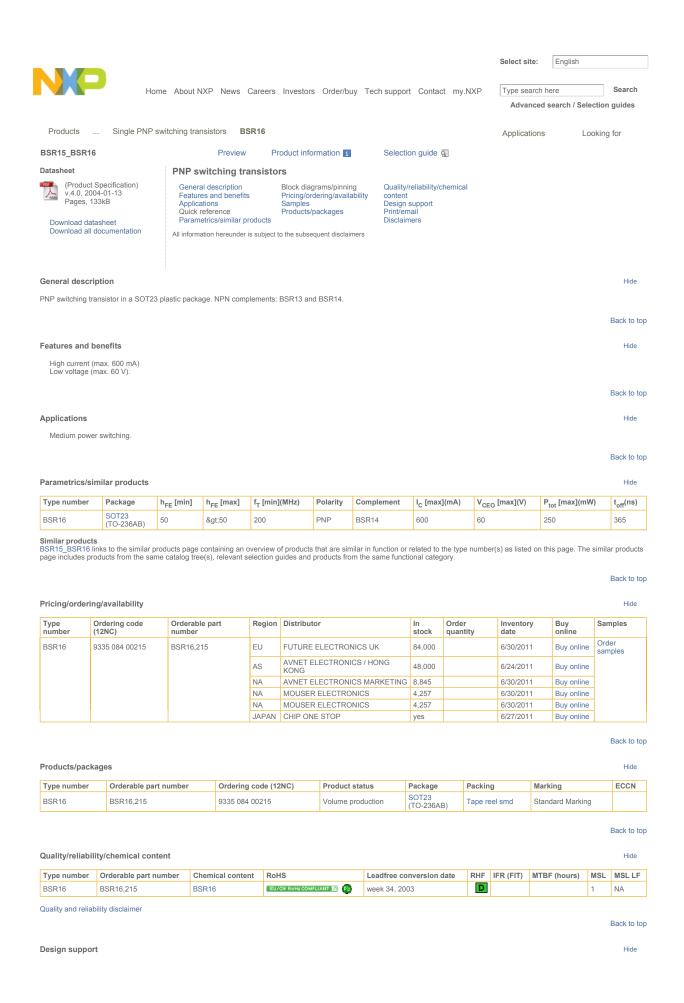
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Other type Letter Symbols - Transistors; General (v.1.0, 1999-05-07)	
Support Models Spice model of BSR16	
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