# International Rectifier

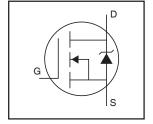
### **AUTOMOTIVE GRADE**

## **AUIRFZ44V**

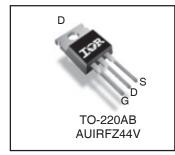
HEXFET® Power MOSFET

### **Features**

- Advanced Planar Technology
- Low On-Resistance
- Dynamic dV/dT Rating
- 175°C Operating Temperature
- Fast Switching
- Fully Avalanche Rated
- Repetitive Avalanche Allowed up to Tjmax
- Lead-Free, RoHS Compliant
- Automotive Qualified \*



V <sub>(BR)DSS</sub>		60V
R <sub>DS(on)</sub>	max.	<b>16.5m</b> $Ω$
I <sub>D</sub>		55A



G	D	S
Gate	Drain	Source

### **Description**

Specifically designed for Automotive applications, this stripe planar design of HEXFET® Power MOSFETs utilizes the latest processing techniques to achieve low on-resistance per silicon area. This benefit combined with the fast switching speed and ruggedized device design that HEXFET power MOSFETs are well known for, provides the designer with an extremely efficient and reliable device for use in Automotive and a wide variety of other applications.

### **Absolute Maximum Ratings**

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only; and functional operation of the device at these or any other condition beyond those indicated in the specifications is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability. The thermal resistance and power dissipation ratings are measured under board mounted and still air conditions. Ambient temperature  $(T_A)$  is 25°C, unless otherwise specified.

	Parameter	Max.	Units
I <sub>D</sub> @ T <sub>C</sub> = 25°C	Continuous Drain Current, V <sub>GS</sub> @ 10V	55	
I <sub>D</sub> @ T <sub>C</sub> = 100°C	Continuous Drain Current, V <sub>GS</sub> @ 10V	39	Α
I <sub>DM</sub>	Pulsed Drain Current ①	220	
P <sub>D</sub> @T <sub>C</sub> = 25°C	Power Dissipation	115	W
	Linear Derating Factor	0.77	W/°C
$V_{GS}$	Gate-to-Source Voltage	±20	V
E <sub>AS</sub>	Single Pulse Avalanche Energy ②	115	mJ
I <sub>AR</sub>	Avalanche Current ①	55	Α
E <sub>AR</sub>	Repetitive Avalanche Energy ①	11	mJ
dv/dt	Peak Diode Recovery dv/dt 3	4.5	V/ns
TJ	Operating Junction and	-55 to + 175	
T <sub>STG</sub>	Storage Temperature Range	-55 10 + 175	°C
	Soldering Temperature, for 10 seconds	300 (1.6mm from case )	
	Mounting Torque, 6-32 or M3 screw	10 lbf•in (1.1N•m)	

#### **Thermal Resistance**

	Parameter	Тур.	Max.	Units
$R_{\theta JC}$	Junction-to-Case		1.3	
$R_{\theta CS}$	Case-to-Sink, Flat, Greased Surface	0.50		°C/W
$R_{\theta JA}$	Junction-to-Ambient (PCB mounted)		62	

HEXFET® is a registered trademark of International Rectifier.

<sup>\*</sup>Qualification standards can be found at http://www.irf.com/

### Static Electrical Characteristics @ $T_J = 25$ °C (unless otherwise specified)

	Parameter	Min.	Тур.	Max.	Units	Conditions
V <sub>(BR)DSS</sub>	Drain-to-Source Breakdown Voltage	60			V	$V_{GS} = 0V, I_D = 250\mu A$
$\Delta V_{(BR)DSS}/\Delta T_J$	Breakdown Voltage Temp. Coefficient		0.062		V/°C	Reference to 25°C, I <sub>D</sub> = 1mA
R <sub>DS(on)</sub>	Static Drain-to-Source On-Resistance			16.5	mΩ	V <sub>GS</sub> = 10V, I <sub>D</sub> = 31A ④
$V_{GS(th)}$	Gate Threshold Voltage	2.0		4.0	V	$V_{DS} = V_{GS}$ , $I_D = 250\mu A$
gfs	Forward Transconductance	24			S	$V_{DS} = 25V, I_{D} = 31A^{\textcircled{4}}$
I <sub>DSS</sub>	Drain-to-Source Leakage Current			25	μΑ	$V_{DS} = 60V, V_{GS} = 0V$
				250		$V_{DS} = 48V, V_{GS} = 0V, T_{J} = 150^{\circ}C$
I <sub>GSS</sub>	Gate-to-Source Forward Leakage			100	nA	V <sub>GS</sub> = 20V
	Gate-to-Source Reverse Leakage			-100		V <sub>GS</sub> = -20V

### Dynamic Electrical Characteristics @ T<sub>J</sub> = 25°C (unless otherwise specified)

- ya		• (			- 10	
$Q_g$	Total Gate Charge			67		I <sub>D</sub> = 51A
$Q_{gs}$	Gate-to-Source Charge			18	nC	$V_{DS} = 48V$
$Q_{gd}$	Gate-to-Drain ("Miller") Charge			25		V <sub>GS</sub> = 10V, See Fig.6 and 13 <sup>⊕</sup>
t <sub>d(on)</sub>	Turn-On Delay Time		13			$V_{DD} = 30V$
t <sub>r</sub>	Rise Time		97			I <sub>D</sub> = 51A
t <sub>d(off)</sub>	Turn-Off Delay Time		40		ns	$R_G = 9.1\Omega$
t <sub>f</sub>	Fall Time		57			$R_D = 0.6\Omega$ , See Fig.10 $\oplus$
L <sub>D</sub>	Internal Drain Inductance		4.5		nH	Between lead, 6mm (0.25in.)
L <sub>S</sub>	Internal Source Inductance		7.5			Between lead, and center of die contact
C <sub>iss</sub>	Input Capacitance		1812			V <sub>GS</sub> = 0V
Coss	Output Capacitance		393		pF	$V_{DS} = 25V$
C <sub>rss</sub>	Reverse Transfer Capacitance		103			f = 1.0MHz, See Fig.5

### **Diode Characteristics**

	Parameter	Min.	Тур.	Max.	Units	Conditions	
I <sub>S</sub>	Continuous Source Current			55		MOSFET symbol	
	(Body Diode)		55		Α	showing the	
I <sub>SM</sub>	Pulsed Source Current			220		integral reverse	
	(Body Diode) ①		22			p-n junction diode.	
$V_{SD}$	Diode Forward Voltage			2.5	V	$T_J = 25^{\circ}C$ , $I_S = 51A$ , $V_{GS} = 0V$ ④	
t <sub>rr</sub>	Reverse Recovery Time		70	105	ns	T <sub>J</sub> = 25°C, I <sub>F</sub> = 51A	
Q <sub>rr</sub>	Reverse Recovery Charge		146	219	nC	di/dt = 100A/µs <sup>④</sup>	
t <sub>on</sub>	Forward Turn-On Time	Intrinsic	Intrinsic turn-on time is negligible (turn-on is dominated by LS+LD)				

#### Notes:

- @ Starting  $T_J = 25^{\circ}C,\, L = 89\mu H,\, R_G = 25\Omega,\, I_{AS} = 51A.$  (See Figure 12)
- $\ensuremath{ \Im \ } I_{SD} \leq 51 A, \ di/dt \leq 227 A/\mu s, \ V_{DD} \leq V_{(BR)DSS}, \ T_J \leq 175 ^{\circ} C$
- 4 Pulse width  $\leq$  300 $\mu$ s; duty cycle  $\leq$  2%.

### Qualification Information<sup>†</sup>

		Automotive (per AEC-Q101) ††			
		Comments: This part number(s) passed Automotive qualification IR's Industrial and Consumer qualification level is granted by extension of the higher Automotive level.			
Moisture S	Sensitivity Level	3L-TO-220 N/A			
	Machine Model		Class M3(+/- 400V ) <sup>†††</sup> (per AEC-Q101-002)		
ESD Human Body Model		Class H1B(+/- 1000V ) <sup>†††</sup> (per AEC-Q101-001)			
	Charged Device Model	Class C5(+/- 2000V) <sup>†††</sup> (per AEC-Q101-005)			
RoHS Compliant		Yes			

<sup>†</sup> Qualification standards can be found at International Rectifier's web site: http://www.irf.com/

††† Highest passing voltage

<sup>††</sup> Exceptions (if any) to AEC-Q101 requirements are noted in the qualification report.

### **AUIRFZ44V**

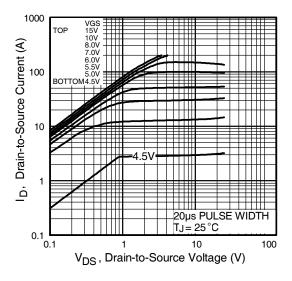


Fig 1. Typical Output Characteristics

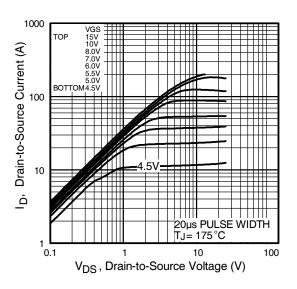


Fig 2. Typical Output Characteristics

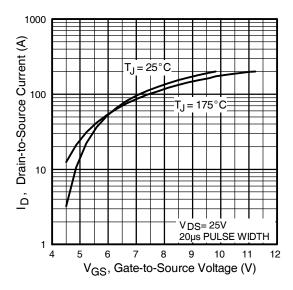
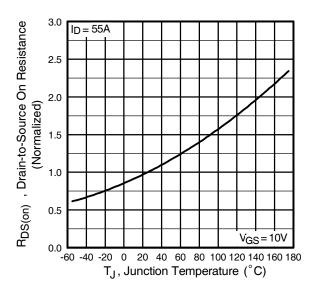
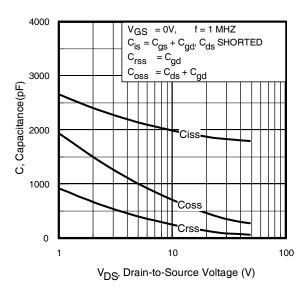


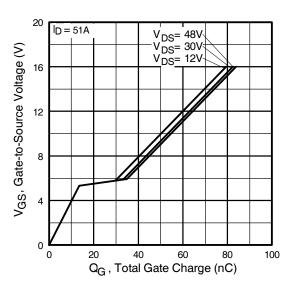
Fig 3. Typical Transfer Characteristics



**Fig 4.** Normalized On-Resistance Vs. Temperature



**Fig 5.** Typical Capacitance Vs. Drain-to-Source Voltage



**Fig 6.** Typical Gate Charge Vs. Gate-to-Source Voltage

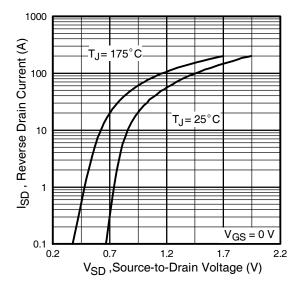


Fig 7. Typical Source-Drain Diode Forward Voltage

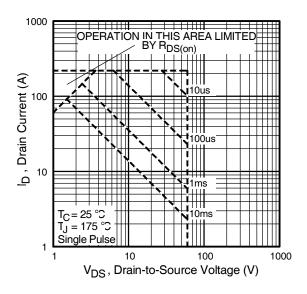


Fig 8. Maximum Safe Operating Area

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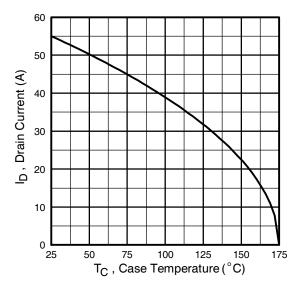


Fig 9. Maximum Drain Current Vs.
Case Temperature

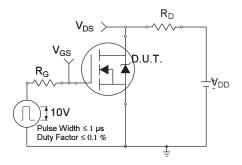


Fig 10a. Switching Time Test Circuit

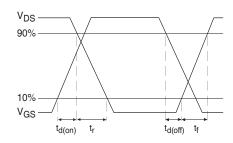


Fig 10b. Switching Time Waveforms

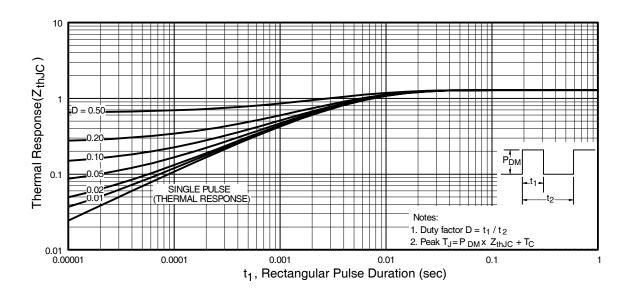


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Case

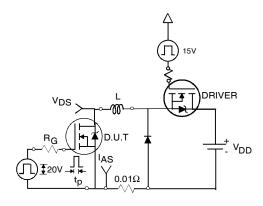


Fig 12a. Unclamped Inductive Test Circuit

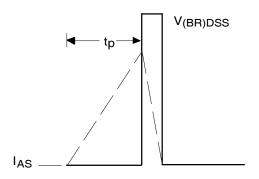


Fig 12b. Unclamped Inductive Waveforms

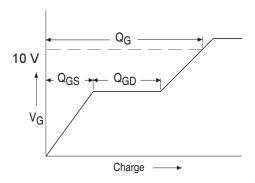


Fig 13a. Basic Gate Charge Waveform

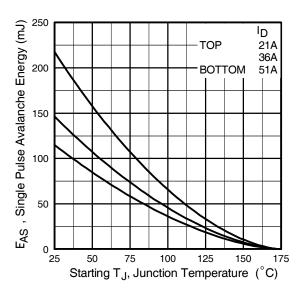


Fig 12c. Maximum Avalanche Energy Vs. Drain Current

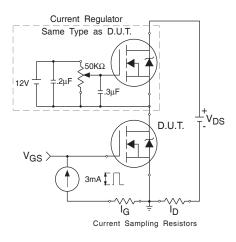


Fig 13b. Gate Charge Test Circuit

### **AUIRFZ44V**

### Peak Diode Recovery dv/dt Test Circuit

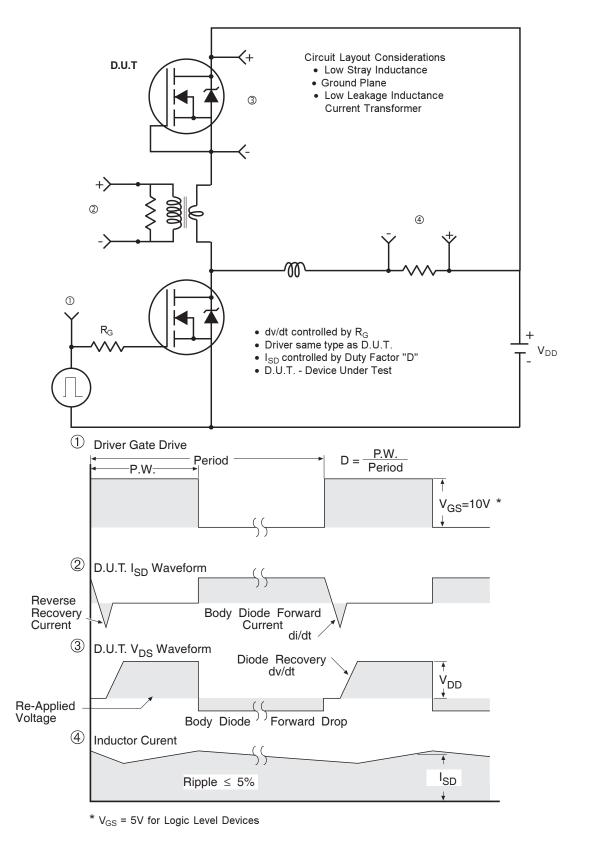
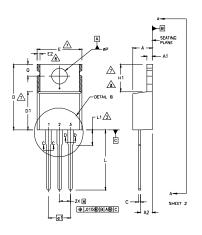
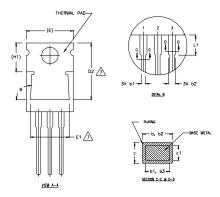


Fig 14. For N-Channel HEXFETS

### TO-220AB Package Outline

Dimensions are shown in millimeters (inches)





#### NOTES:

- DIMENSIONING AND TOLERANCING PER ASME Y14.5 M- 1994. DIMENSIONS ARE SHOWN IN INCHES [MILLIMETERS].
- LEAD DIMENSION AND FINISH UNCONTROLLED IN LI
- DIMENSION D & E DO NOT INCLUDE MOLD FLASH, MOLD FLASH SHALL NOT EXCEED .005" (0.127) PER SIDE. THESE DIMENSIONS ARE

MEASURED AT THE OUTERMOST EXTREMES OF THE PLASTIC BODY. DIMENSION 61 & c1 APPLY TO BASE METAL ONLY.

- CONTROLLING DIMENSION ; INCHES,
- THERMAL PAD CONTOUR OPTIONAL WITHIN DIMENSIONS E,H1,D2 & E1
- DIMENSION E2 X H1 DEFINE A ZONE WHERE STAMPING AND SINGULATION IRREGULARITIES ARE ALLOWED.

SYMBOL	MILLIM	ETERS	INC	INCHES	
	MIN.	MAX.	MIN.	MAX.	NOTES
A	3.56	4.82	.140	.190	
A1	0.51	1.40	.020	.055	
A2	2.04	2.92	.080	.115	
b	0.38	1,01	.015	.040	
b1	0.38	0.96	.015	.038	5
b2	1,15	1,77	.045	.070	
b3	1.15	1.73	.045	.068	
С	0.36	0.61	.014	.024	
c1	0.36	0.56	.014	.022	5
D	14,22	16.51	.560	.650	4
D1	8.38	9.02	.330	.355	
D2	12.19	12.88	.480	.507	7
E	9.66	10.66	.380	.420	4,7
E1	8,38	8.89	.330	.350	7
е		BSC	.100	.100 BSC	
e1	5.	08	.200	,200 BSC	
H1	5,85	6,55	.230	.270	7,8
L	12.70	14.73	.500	.580	
L1	-	6,35	-	.250	3
øΡ	3,54	4.08	.139	,161	
Q	2,54	3.42	.100	.135	
Ø	90*-	-93*	90*-	-93*	

#### LEAD ASSIGNMENTS

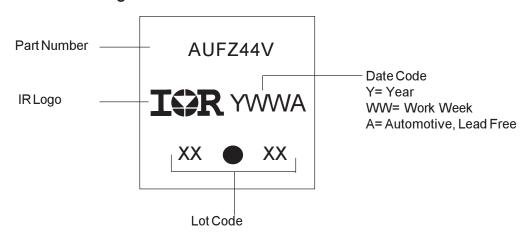
- HEXFET
- 1.- GATE 2.- DRAIN 3.- SOURCE

#### IGBTs, CoPACK

1.- GATE 2.- COLLECTOR 3.- EMITTER

- DIODES
- 1,- ANODE/OPEN 2.- CATHODE 3.- ANODE

### TO-220AB Part Marking Information



Note: For the most current drawing please refer to IR website at http://www.irf.com/package/

**AUIRFZ44V** 

### **Ordering Information**

Base part	Package Type	Standard Pack		Complete Part Number
		Form	Quantity	
AUIRFZ44V	TO-220	Tube	50	AUIRFZ44V

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For technical support, please contact IR's Technical Assistance Center

http://www.irf.com/technical-info/

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